

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TLV3401CD	ACTIVE	SOIC	D	8	75	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	3401C	<a href="#">Samples</a>
TLV3401CDBVR	OBSOLETE	SOT-23	DBV	5		TBD	Call TI	Call TI	0 to 70	VBDC	
TLV3401CDBVT	OBSOLETE	SOT-23	DBV	5		TBD	Call TI	Call TI	0 to 70	VBDC	
TLV3401ID	ACTIVE	SOIC	D	8	75	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	3401I	<a href="#">Samples</a>
TLV3401IDBVR	ACTIVE	SOT-23	DBV	5	3000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	VBDI	<a href="#">Samples</a>
TLV3401IDBVRG4	ACTIVE	SOT-23	DBV	5	3000	TBD	Call TI	Call TI	-40 to 125		<a href="#">Samples</a>
TLV3401IDBVT	OBSOLETE	SOT-23	DBV	5		TBD	Call TI	Call TI	-40 to 125	VBDI	
TLV3401IDR	ACTIVE	SOIC	D	8	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	3401I	<a href="#">Samples</a>
TLV3401IP	ACTIVE	PDIP	P	8	50	RoHS & Green	NIPDAU	N / A for Pkg Type	-40 to 125	TLV3401I	<a href="#">Samples</a>
TLV3402CD	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI	0 to 70	3402C	
TLV3402CDGK	OBSOLETE	VSSOP	DGK	8		TBD	Call TI	Call TI	0 to 70	AJJ	
TLV3402CDGKR	OBSOLETE	VSSOP	DGK	8		TBD	Call TI	Call TI	0 to 70	AJJ	
TLV3402CDR	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI	0 to 70	3402C	
TLV3402ID	OBSOLETE	SOIC	D	8		TBD	Call TI	Call TI	-40 to 125	3402I	
TLV3402IDGK	OBSOLETE	VSSOP	DGK	8		TBD	Call TI	Call TI	-40 to 125	AJK	
TLV3402IDGKR	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	NIPDAU   NIPDAUAG	Level-1-260C-UNLIM	-40 to 125	AJK	<a href="#">Samples</a>
TLV3402IDR	ACTIVE	SOIC	D	8	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	3402I	<a href="#">Samples</a>
TLV3402IDRG4	ACTIVE	SOIC	D	8	2500	TBD	Call TI	Call TI	-40 to 125		<a href="#">Samples</a>
TLV3402IP	ACTIVE	PDIP	P	8	50	RoHS & Green	NIPDAU	N / A for Pkg Type	-40 to 125	TLV3402I	<a href="#">Samples</a>
TLV3404CD	OBSOLETE	SOIC	D	14		TBD	Call TI	Call TI	0 to 70	3404C	
TLV3404CDR	OBSOLETE	SOIC	D	14		TBD	Call TI	Call TI	0 to 70	3404C	
TLV3404CPWR	ACTIVE	TSSOP	PW	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	3404C	<a href="#">Samples</a>
TLV3404ID	OBSOLETE	SOIC	D	14		TBD	Call TI	Call TI	-40 to 125	3404I	
TLV3404IDR	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	3404I	<a href="#">Samples</a>

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TLV3404IN	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	-40 to 125	TLV3404I	<a href="#">Samples</a>
TLV3404IPW	OBSOLETE	TSSOP	PW	14		TBD	Call TI	Call TI	-40 to 125	3404I	
TLV3404IPWR	ACTIVE	TSSOP	PW	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	-40 to 125	3404I	<a href="#">Samples</a>

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

**RoHS Exempt:** TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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