

TMAG6181-Q1 具有集成圈数计数器的高精度模拟 AMR 角度传感器

1 特性

- 符合面向汽车应用的 AEC-Q100 标准：
 - 温度等级 0：-40°C 至 150°C
- 功能安全合规型
 - 专为功能安全应用开发
 - 有助于使 ISO 26262 系统设计满足 ASIL-B 要求的文档
- 高精度、低角度误差 AMR 传感器：
 - 0.1° (典型值)
 - 0.4° (在 3.3V 下、整个温度范围内的最大值)
 - 0.56° (在 5.5V 下、整个温度范围内的最大值)
- 高速 AMR 角度传感器：
 - < 2μs 的超低延迟支持高达 100krpm
 - 角度范围：180°
- 具有低角度漂移，无需在整个温度范围内进行校准
- 宽工作磁场范围：20mT 至 1T
- 正弦和余弦差分比例式模拟输出
- 支持差分端或单端应用
- 快速启动时间：< 40μs
- 集成的圈数计数器使用 PWM 输出提供旋转圈数计数：
 - 启用圈数计数器的低功耗模式为 50 μA
 - 智能旋转跟踪功能可在低功耗模式下跟踪高达 8krpm 的旋转
- 用于进入睡眠模式的专用引脚：< 5μA
- 电源电压范围：2.7V 至 5.5V

2 应用

- EPS 转子位置检测
- EPS 手轮角度检测
- BLDC/PMSM 转子位置检测
- 电动自行车和两轮机动车
- 雨刮器模块和执行器
- 编码器位置检测
- 伺服驱动位置传感器
- 集成带式起动发电机

3 说明

TMAG6181-Q1 是一款基于各向异性磁阻 (AMR) 技术的高精度角度传感器。该器件集成信号调节放大器，并提供与所施加平面磁场的方向相关的差分正弦和余弦模拟输出。该器件还在 X 轴和 Y 轴上具有两个独立的霍尔传感器，用于跟踪旋转情况。

TMAG6181-Q1 具有宽工作磁场 (20mT - 1T)，可实现灵活的机械放置以及低延迟 (< 2μs) 输出，适用于转子位置检测等高速应用。通过使用一个睡眠引脚，使器件能够进入两种低功耗模式 (睡眠模式和低功耗圈数计数器模式)，快速启动时间 (<40μs) 有助于实现低功耗应用。该器件集成一个圈数计数器来记录旋转圈数，并使用 TURNS 引脚上的 PWM 来传输圈数计数器值。

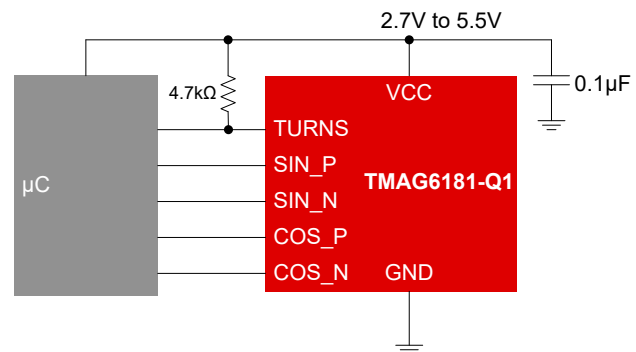
TMAG6181-Q1 提供广泛的诊断功能，专为功能安全应用而设计。该器件可在 -40°C 至 150°C 的宽环境温度范围内保持稳定一致的性能，同时具有超小的热漂移和寿命误差。

封装信息

器件型号	封装 ⁽¹⁾	封装尺寸 ⁽²⁾
TMAG6181-Q1	DGK (VSSOP, 8)	3.00mm × 4.90mm

(1) 有关所有可选封装，请参阅节 10。

(2) 封装尺寸 (长 × 宽) 为标称值，并包括引脚 (如适用)。



应用方框图



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4 Pin Configuration and Functions

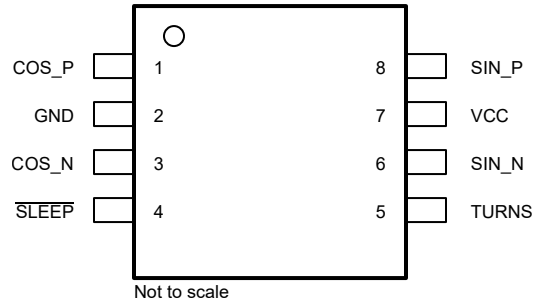


图 4-1. DGK Package 8-Pin VSSOP Top View

表 4-1. Pin Functions

PIN		TYPE ⁽¹⁾	DESCRIPTION
NO.	NAME		
1	COS_P	O	Differential cosine output (positive)
2	GND	G	Ground reference
3	COS_N	O	Differential cosine output (negative)
4	SLEEP	I	SLEEP pin (active low)
5	TURNS	I/O	Turns counter output or Reset input (open drain)
6	SIN_N	O	Differential sine output (negative)
7	VCC	P	Power supply
8	SIN_P	O	Differential sine output (positive)

(1) I = input, O = output, I/O = input and output, G = ground, P = power

5 Specifications

5.1 Absolute Maximum Ratings

over operating free-air temperature range (unless otherwise noted)⁽¹⁾

		MIN	MAX	UNIT
V _{CC}	Main supply voltage	- 0.3	7	V
I _{OUT}	Output current (SIN_P, SIN_N, COS_P, COS_N, TURNS)	- 10	10	mA
V _{OUT}	Output voltage (SIN_P, SIN_N, COS_P, COS_N, TURNS)	- 0.3	7	V
V _{IN}	Input voltage SLEEP	- 0.3	V _{VCC} + 0.3	V
T _J	Junction temperature	- 40	170	°C
T _{stg}	Storage temperature	- 65	150	°C

- (1) Operation outside the Absolute Maximum Ratings may cause permanent device damage. Absolute maximum ratings do not imply functional operation of the device at these or any other conditions beyond those listed under Recommended Operating Conditions. If briefly operating outside the Recommended Operating Conditions but within the Absolute Maximum Ratings, the device may not sustain damage, but it may not be fully functional. Operating the device in this manner may affect device reliability, functionality, performance, and shorten the device lifetime.

5.2 ESD Ratings

			VALUE	UNIT	
V _(ESD)	Electrostatic discharge	Human body model (HBM), per AEC Q100-002 ⁽¹⁾ HBM ESD classification level 2	±2000	V	
		Charged device model (CDM), per AEC Q100-011 CDM ESD classification level C4B	All pins		±500
			Corner pins (1, 4, 5, and 8)		±750

- (1) AEC Q100-002 indicates that HBM stressing shall be in accordance with the ANSI/ESDA/JEDEC JS-001 specification.

5.3 Recommended Operating Conditions

over operating free-air temperature range (unless otherwise noted)

		MIN	NOM	MAX	UNIT
V _{CC}	Main supply voltage	2.7		5.5	V
T _A	Operating free air temperature	- 40		150	C
C _L	Capacitive load on SIN_P, SIN_N, COS_P, COS_N	0.1		10	nF
I _L	Current load on SIN_P, SIN_N, COS_P, COS_N	- 1		1	mA
B	Magnetic flux density for AMR saturation	20			mT

5.4 Thermal Information

THERMAL METRIC ⁽¹⁾		TMAG6181-Q1	UNIT
		DGK (VSSOP)	
		8 PINS	
R _{θJA}	Junction-to-ambient thermal resistance	166.8	°C/W
R _{θJC(top)}	Junction-to-case (top) thermal resistance	57.8	°C/W
R _{θJB}	Junction-to-board thermal resistance	88.7	°C/W
Ψ _{JT}	Junction-to-top characterization parameter	7.0	°C/W
Ψ _{JB}	Junction-to-board characterization parameter	87.1	°C/W

- (1) For more information about traditional and new thermal metrics, see the [Semiconductor and IC Package Thermal Metrics](#) application report.

5.5 Electrical Characteristics

over operating free-air temperature range (unless otherwise noted); typical specifications are at $T_A = 25^\circ\text{C}$

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
AMR Output Parameters						
V_{out}	Single-ended output voltage peak to peak	$V_{CC} = 3.3\text{V}$	57	62	67.5	% V_{CC}
		$V_{CC} = 5.5\text{V}$	55	60	65	
k	Amplitude asynchronism ratio ($V_{pk} \cos / V_{pk} \sin$)	$B = 30\text{mT}, V_{CC} = 3.3\text{V}$	- 2.3	0.3	2.3	%
		$B = 30\text{mT}, V_{CC} = 5\text{V}$	- 2.4	0.3	2.4	
$V_{offset_room}^{(1)}$	Differential offset of SIN/COS outputs at room	$B = 30\text{mT}, T_A = 25^\circ\text{C}, V_{CC} = 3.3\text{V}$	- 56		56	mV
		$B = 30\text{mT}, T_A = 25^\circ\text{C}, V_{CC} = 5\text{V}$	- 90		90	
V_{offset_tc}	Temperature coefficient of differential offset voltage	$B = 30\text{mT}, V_{CC} = 3.3\text{V}$		± 0.1		mV/ $^\circ\text{C}$
		$B = 30\text{mT}, V_{CC} = 5\text{V}$		± 0.1		
V_{CM}	Common-mode output voltage	$B = 30\text{mT}, V_{CC} = 3.3\text{V}$	48	50	52	% V_{CC}
		$B = 30\text{mT}, V_{CC} = 5\text{V}$	48	50	52	
V_{NOISE}	Output referred noise (differential)	$B = 30\text{mT}, C_{load} = 100\text{pF}$		0.5		mV $_{rms}$
R_{out}	Series output resistance			55		Ω
R_{out_sleep}	Series output resistance during Sleep	SLEEP = GND	1			M Ω
$t_{agc_update}^{(2)}$	Update rate of the automatic gain control	After V_{out} reaching 60% of V_{CC}		1		s
DC Power						
V_{CC_UV}	VCC undervoltage threshold			2.45	2.65	V
V_{CC_OV}	VCC overvoltage threshold			5.9	6.36	
I_{ACT}	Active mode current from VCC	SLEEP = VCC		5	10	mA
I_{DCM_SLEEP}	Sleep mode current from VCC	DCM mode enabled		50		μA
I_{SLEEP}	Sleep mode current from VCC	SLEEP = GND		4.5		μA
I_{LP}	Average current during low power mode from VCC	Low power DCM mode with turns counter enabled (no rotations detected)		50		μA
$t_{sleep_no_rotation}$	Sleep time during low power mode when the magnetic field is static (not rotating)	$B = 30\text{ mT}$		25		ms
$t_{on_startup}$	Power-on time during start-up	To achieve 90% of output voltages after VCC has reached final value ($C_{LOAD} = 100\text{pF}$)		38	85	μs
t_{on_sleep}	Power on time after SLEEP goes high	To achieve 90% of output voltages after SLEEP > V_{IH} ($C_{LOAD} = 100\text{pF}$)		45	50	μs
t_{sleep_pd}	Time that SLEEP must stay low when transitioning from active mode to low power mode		125		400	μs
$t_{sleep_timeout}$	Timeout between two consecutive pulses on SLEEP pin when entering low power mode		25		400	μs
t_{sleep_mode}	Time that SLEEP must stay low to enter sleep mode		1.1			ms
Digital I/O						
V_{IH}	High level input voltage	SLEEP	$0.65 \times V_{CC}$			V
V_{IL}	Low level input voltage				$0.3 \times V_{CC}$	

over operating free-air temperature range (unless otherwise noted); typical specifications are at $T_A = 25^\circ\text{C}$

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
V_{IH}	High level input voltage	TURNS	$0.65 \times V_{CC}$			V
V_{IL}	Low level input voltage				$0.3 \times V_{CC}$	V
V_{OL}	Low level output voltage	$I_O = 2\text{mA}$ on TURNS pin	0		0.4	V
Turns Counter						
f_{PWM}	PWM carrier frequency	When Turns Counter is enabled		2.5		KHz
DC_{PWM}	Output Valid Duty Cycle Range		10		90	%
TC	Turns Counter Range		- 1024		1023	
TCstep	Turns Counter PWM Step Size			0.039		% / Turn
TC_PWM _Q	Quiescent Duty Cycle	Turns Counter = 0		50		%
TC_PWM _{QΔL}	Quiescent Duty Cycle Lifetime drift			0.5		%
TC _{noise}	RMS noise on PWM duty cycle of TURNS pin			0.005		%
T_{tc_start}	Minimum Time required to pull down the TURNS pin to initiate the turns counter				125	μs
T_{tc_reset}	Minimum Time required to pull down the TURNS pin to reset the turns counter				1.1	ms
T_{tc_delay}	Time delay from rising edge on TURNS pin to the first PWM falling edge			55		μs

- (1) V_{offset_room} is the differential offset voltage deviation from the target of 0mV.
- (2) The AGC rate initially starts at 4 μs on power-up. When V_{OUT} crosses 60% of V_{CC} the updated rate switches to 1s.

5.6 Magnetic Characteristics

over operating free-air temperature range (unless otherwise noted); typical specifications are at TA = 25°C

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
Angular Performance						
ANG _{ERR_DYN_SE}	Angular error linearity across temperature on continuous calibration (gain / offset) (single ended)	B = 30mT, V _{CC} = 3.3V, Magnetic field Rotation Speed = 1000rpm		0.1	0.35	deg
		B = 30mT, V _{CC} = 5V, Magnetic field Rotation Speed = 1000rpm		0.1	0.56	deg
ANG _{ERR_DYN_DE}	Angular error linearity across temperature on continuous calibration (gain / offset) (differential ended)	B = 30mT, V _{CC} = 3.3V, Magnetic field Rotation Speed = 1000rpm		0.1	0.4	deg
		B = 30mT, V _{CC} = 5V, Magnetic field Rotation Speed = 1000rpm		0.1	0.56	deg
ANG _{ERR_RTCAL_SE}	Angular error linearity across temperature after room temperature calibration (of offset / gain mismatch) (single ended)	B = 30mT, V _{CC} = 3.3V, Ideal magnet alignment		0.1	1.1	deg
		B = 30mT, V _{CC} = 5V, Ideal magnet alignment		0.1	1.1	deg
ANG _{ERR_RTCAL_DE}	Angular error linearity across temperature after room temperature calibration (of offset / gain mismatch) (differential ended)	B = 30mT, V _{CC} = 3.3V, Ideal magnet alignment		0.2	1.0	deg
		B = 30mT, V _{CC} = 5V, Ideal magnet alignment		0.1	0.95	deg
ANG _{ERR_NOCAL_SE}	Angular error linearity across temperature with no calibration of gain / offset (single ended)	B = 30mT, V _{CC} = 3.3V, Ideal magnet alignment		0.5	1.52	deg
		B = 30mT, V _{CC} = 5V, Ideal magnet alignment		0.5	1.26	deg
ANG _{ERR_NOCAL_DE}	Angular error linearity across temperature with no calibration of gain / offset (differential ended)	B = 30mT, V _{CC} = 3.3V, Ideal magnet alignment		0.4	1.1	deg
		B = 30mT, V _{CC} = 5V, Ideal magnet alignment		0.4	1.0	deg
ANG _{LT_DRIFT}	Angle error lifetime drift	B = 30mT		0.05	±0.75	deg
ANG _{HYST}	Angle hysteresis error	B = 30mT		0.01	0.06	deg
ANG _{OE_ERR}	Orthogonality error	B = 30mT		0.01	0.051	deg
ANG _{NOISE}	Angular RMS (1-sigma) noise in degrees	B = 30mT, C _{load} = 100pF		0.01	0.06	deg
t _{del_amr}	Propagation Delay time	C _{load} = 100pF		1.6		µs
BW _{3dB_amr}	3-dB Bandwidth	C _{load} = 100pF		100		KHz
φ _{err}	Phase error	Magnetic Field Rotation Speed = 10000rpm, C _{load} = 100pF		0.15		deg
Hall sensor characteristics						
B _{OP(X)} , B _{OP(Y)}	Magnetic field operating point			3		mT
B _{RP(X)} , B _{RP(Y)}	Magnetic field release point			-3		mT
B _{OP} - B _{RP}	Magnetic hysteresis			3.5	6	mT
B _{SYM_OP}	Operating point symmetry	B _{op(x)} - B _{op(y)}		±0.5		mT
B _{SYM_RP}	Release point symmetry	B _{rp(x)} - B _{rp(y)}		±0.5		mT
B _{SYM_RP}	Release point symmetry	B _{rp(x)} - B _{rp(y)}		0		mT
t _{PD_HALL}	Propagation delay time per channel	Change in B _{OP} or B _{RP} to change in output		10		µs

5.7 Typical Characteristics

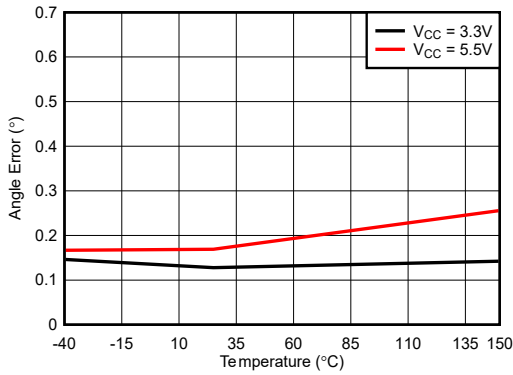


图 5-1. Absolute Angle Error. Differential with Dynamic Calibration

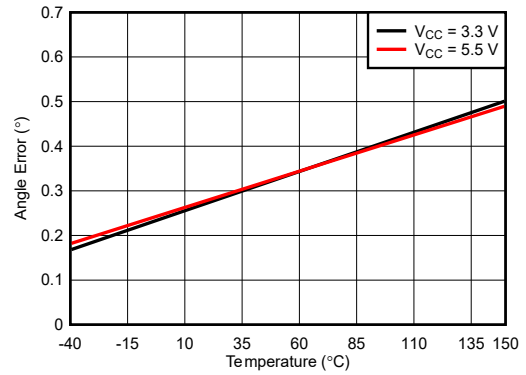


图 5-2. Absolute Angle Error. Differential with Room Temperature Calibration

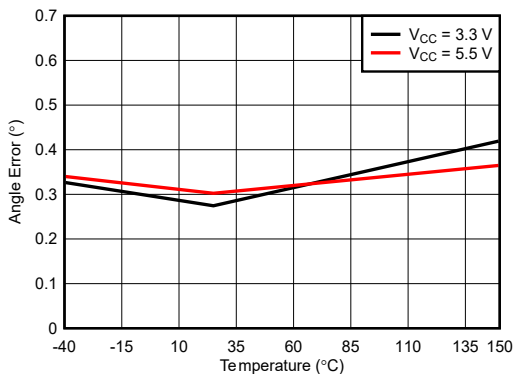


图 5-3. Absolute Angle Error. Differential with No Calibration

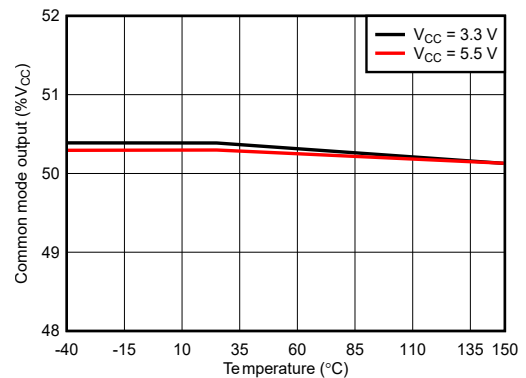


图 5-4. AMR Common-Mode Output Voltage

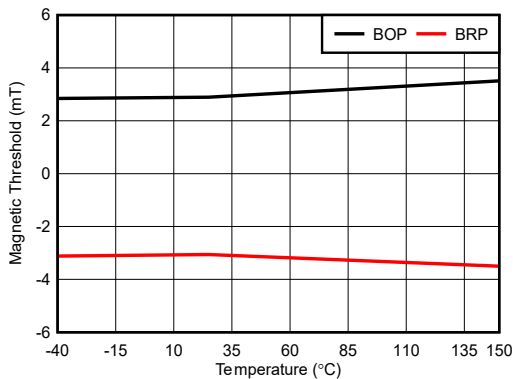


图 5-5. Hall Sensor BOP and BRP Typical Threshold

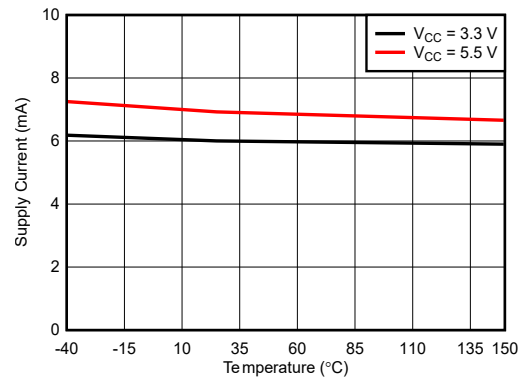
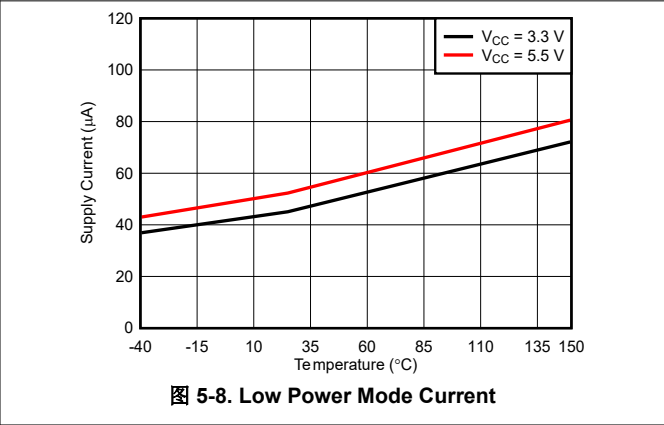
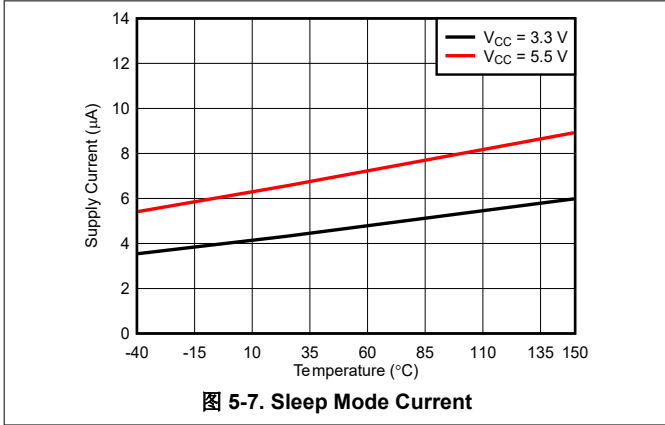


图 5-6. Active Mode Current

5.7 Typical Characteristics (continued)



6 Detailed Description

6.1 Overview

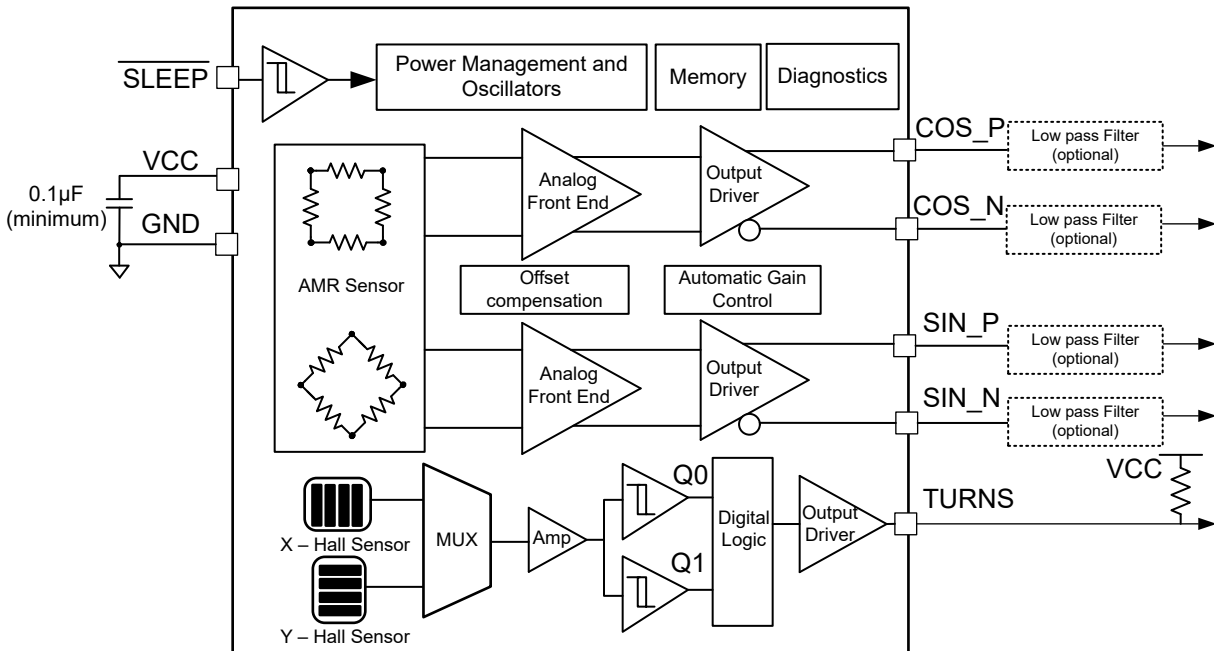
The TMAG6181-Q1 is a high-precision angle sensor based on the AMR sensor technology vertically integrated on top of the integrated amplifiers on silicon. The differential output sine and cosine signals from the AMR sensor are proportional to the angle of the applied magnetic field. They are internally signal conditioned, temperature compensated, and driven by differential output amplifiers with the ability to drive large capacitive loads. The output voltages of the AMR sensor are ratiometric to the supply voltage so the external ADC can use the supply voltage as a reference.

The TMAG6181-Q1 features a $\overline{\text{SLEEP}}$ pin to enable low power operation. The device integrates a rotation turns counter to measure the number of rotations of the external magnetic field using the integrated X and Y Hall sensors at a resolution of 90 degrees. The TURNS pin provides the integrated turns counter output using Pulse Width Modulation (PWM) scheme.

The TMAG6181-Q1 contains the following functional and building blocks:

- The Power Management and Oscillators block contains internal regulators, biasing circuitry, a low-frequency, wake-up oscillator and a high-frequency, wake-up oscillator, overvoltage and undervoltage detection circuitry
- The AMR sensor contains two Wheatstone bridges made of magnetic resistive sensors, each sensing one of the components of the applied magnetic field, the sine and the cosine components.
- The AMR sensing path contains the signal conditioning amplifiers, offset compensation, automatic gain control circuitry and the output drivers.
- The Turns Counting path contains the X and Y Hall sensors, related biasing circuitry, signal conditioning, logic comparators and a counter to keep track of rotations
- The Internal memory block supports the factory-programmed values
- The diagnostic blocks support background diagnostic checks of the internal circuitry

6.2 Functional Block Diagram



6.3 Feature Description

6.3.1 Magnetic Flux Direction

The TMAG6181-Q1 is sensitive to the magnetic field component in X and Y directions. The X and Y fields are in-plane with the package. The device generates sine and cosine outputs from the AMR based on the reference position (0°). See [图 6-1](#).

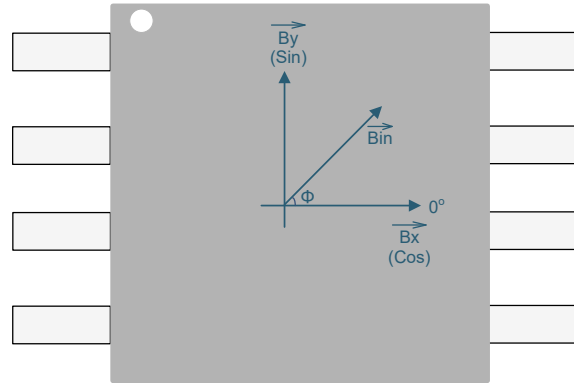


图 6-1. Direction of Sensitivity

6.3.2 Sensors Location and Placement Tolerances

图 6-2 shows the location of the AMR sensor and X, Y Hall elements, along with the placement tolerances inside the TMAG6181-Q1.

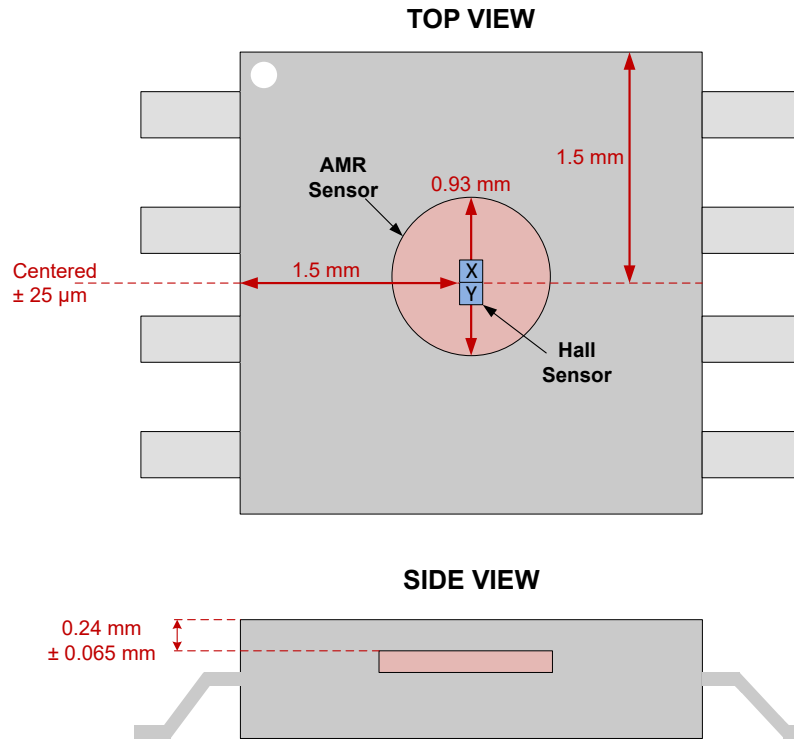


图 6-2. Location of AMR Sensor and Hall Elements

The center of the AMR and Hall sensors lie in the center of the package. 图 6-3 shows the tolerances of the die rotation within the package. This causes a reference angle error (Φ) of $\pm 3^\circ$.

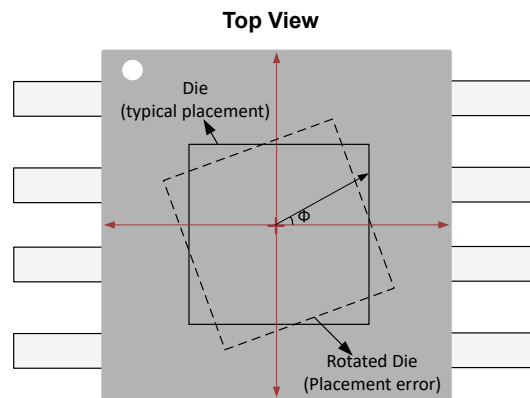


图 6-3. Die Rotation Tolerances in the Package

6.3.3 Magnetic Response

The AMR sensor has two components that are sensitive to the in-plane magnetic field X and Y axes parallel to the chip surface. 图 6-4 shows the AMR sensor with the differential sine and cosine outputs SIN_P, SIN_N, COS_P and COS_N. The outputs have an electrical range of 180 degrees. If the mechanical angle between the sensor reference and the direction of the magnetic field is θ , then the AMR outputs correspond to $\cos 2\theta$ and $\sin 2\theta$, respectively. For every 360° rotation of the external magnetic field, the AMR outputs provide two periods at 180° sensing range for each period. Hence, for a dipole magnet rotating at speed of f , the electrical output from the AMR sensor outputs can be at twice the frequency at $2f$. Use 方程式 1 to calculate the angle of the magnetic field using an arctangent2 function.

$$\theta = \frac{\arctan2\left(\frac{V_{\sin}}{V_{\cos}}\right)}{2} \quad (1)$$

where

- V_{\sin} is the sine output
- V_{\cos} is the cosine output

The AMR sensor is sensitive only to the direction of the magnetic field and has a wide operating magnetic field range. The voltage levels of the AMR outputs are independent of the absolute flux density as long as the magnetic flux density is above the minimum recommended operating fields.

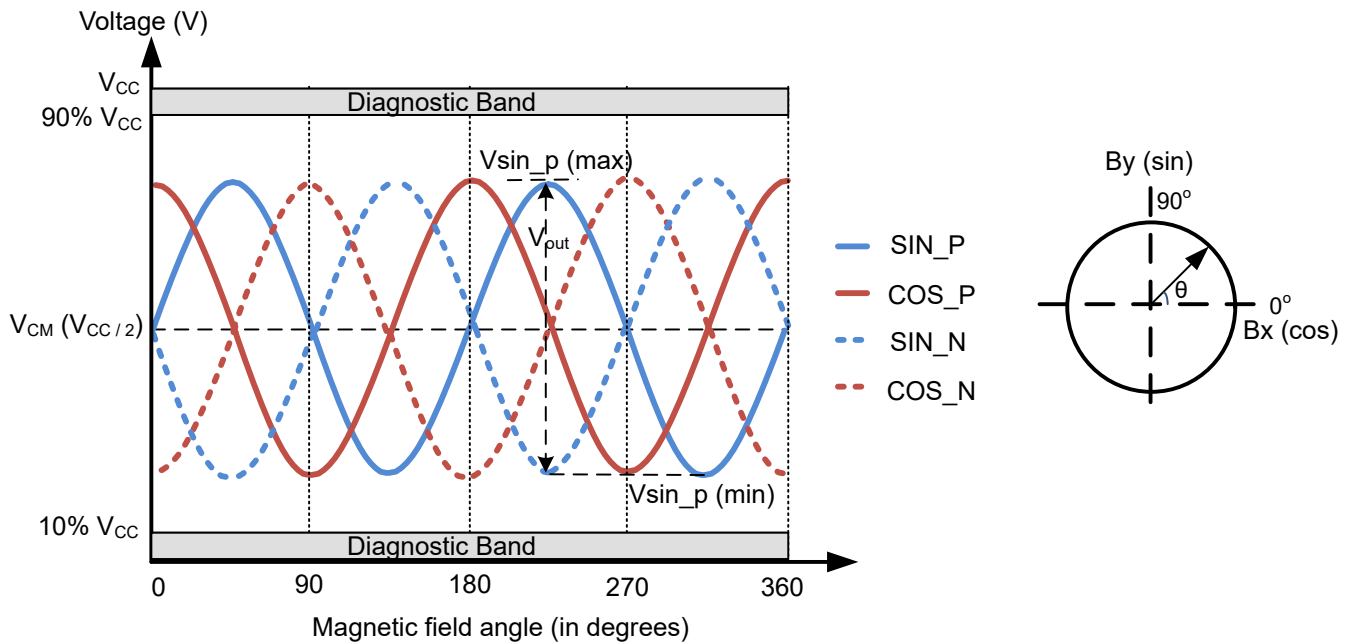


图 6-4. AMR Sensor Outputs Magnetic Response

图 6-5 shows the two integrated Hall sensors X and Y that are sensitive to the in-plane X and Y axes similar to the AMR sensor. The outputs Q1 and Q0 shows the digital outputs of both these sensors, respectively. 图 6-5 shows both the Hall outputs reacting to the input field by going low when the field is higher than operating point (B_{OP}) and going high when the field is lower than returning point (B_{RP}).

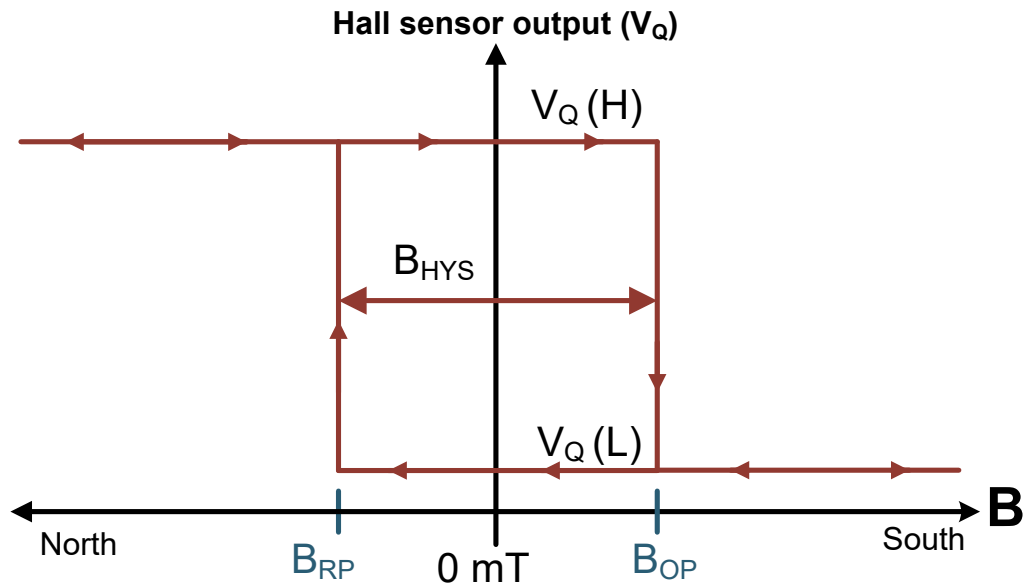


图 6-5. Hall Sensor Magnetic Response

For a rotating input magnetic field, with the Y and X components of B_{SIN} and B_{COS} respectively, [图 6-6](#) shows the response of the AMR and Hall sensors. The integrated X and Y Hall sensors provide digital outputs (Q1 and Q0, respectively). See the [Functional Block Diagram](#). The Hall sensors have a 360° angle range compared to the 180° angle range of the AMR sensors.

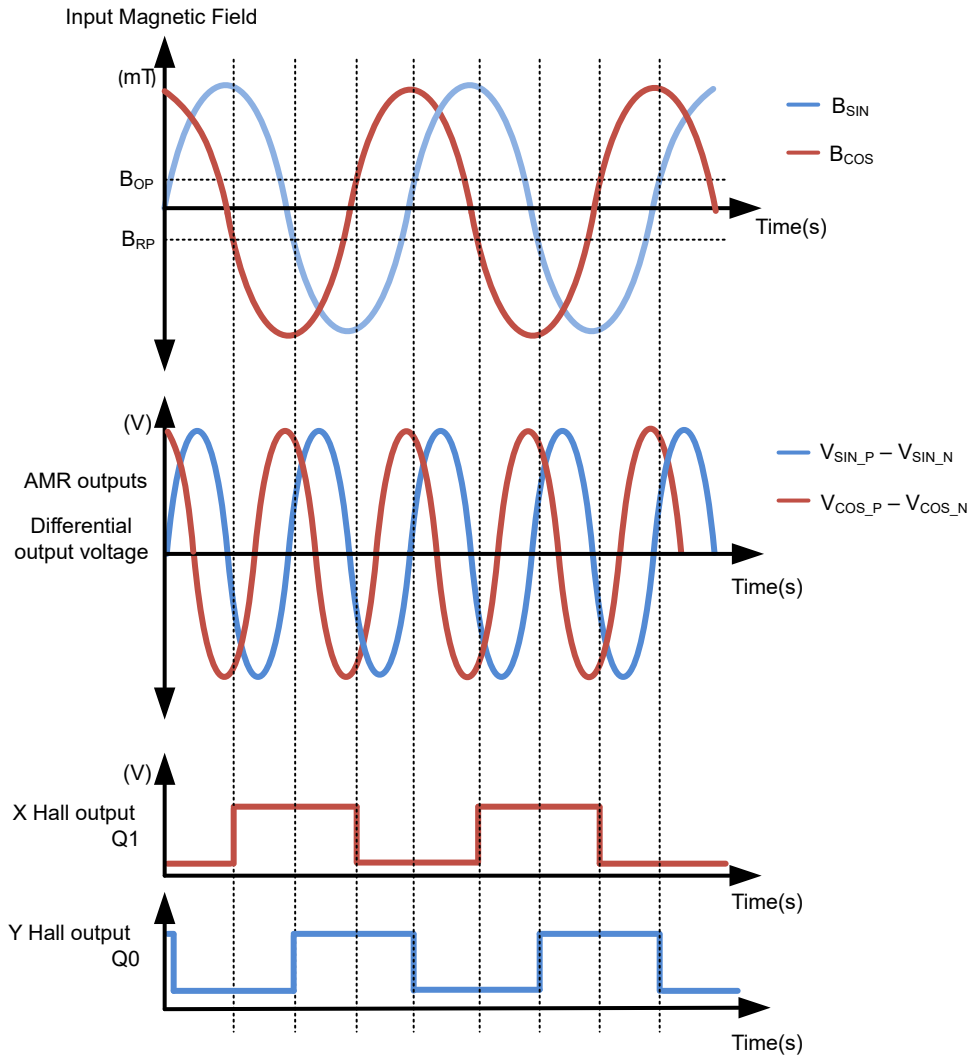


图 6-6. Magnetic Response of AMR and Hall Sensors

6.3.4 Parameters Definition

6.3.4.1 AMR Output Parameters

Magnetic Response shows the single-ended output signals as SIN_P, SIN_N, COS_P and COS_N. These signals are ratiometric to the supply voltage (V_{CC}). The common-mode voltage (V_{CM}) of the individual signals is half of the supply voltage ($V_{CC}/2$). For single-ended signals, V_{OUT} is defined as the difference between the maximum and minimum output voltage for a rotating magnetic field. Use [方程式 2](#) to calculate $V_{OUT_SIN_P}$.

$$V_{OUT_SIN_P} = V_{SIN_P(max)} - V_{SIN_P(min)} \quad (2)$$

where

- $V_{SIN_P(max)}$ is the maximum output voltage across the full magnetic angle range
- $V_{SIN_P(min)}$ is minimum output voltage across the full magnetic angle range

Typically, V_{OUT} is around 60% of the supply voltage (V_{CC}). The diagnostic band indicates that the output signals are outside normal operating range and indicates a presence of fault.

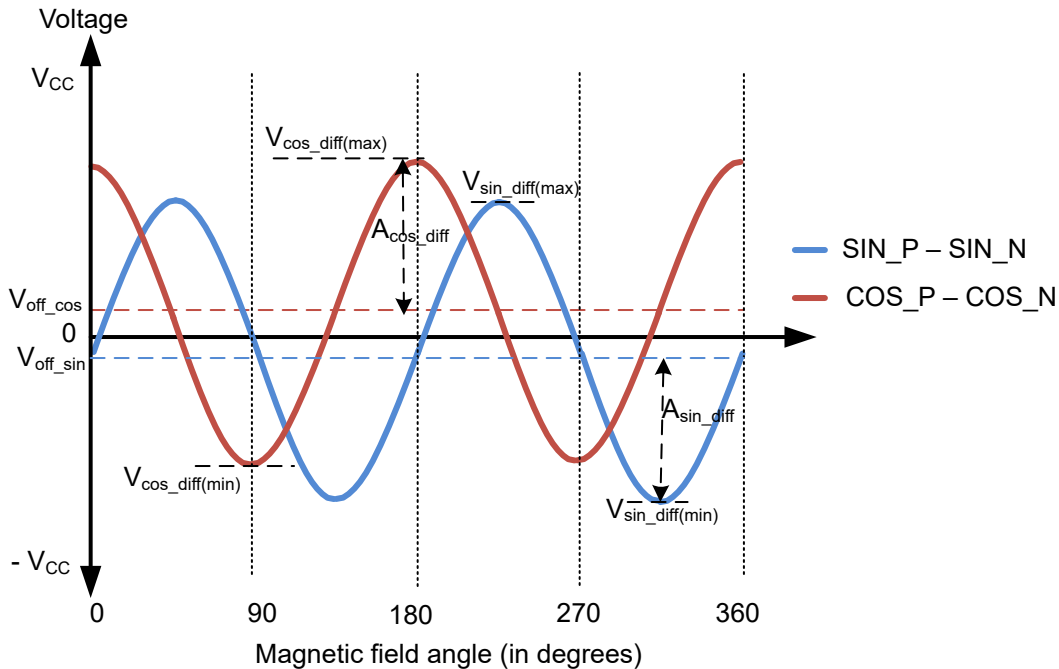


图 6-7. AMR Differential-Ended Output Signals

图 6-7 shows the differential sine and cosine output signals generated from the corresponding sine and cosine single-ended outputs. Use [方程式 3](#) and [方程式 4](#) to calculate the differential voltages.

$$V_{sin_diff} = V_{SIN_P} - V_{SIN_N} \quad (3)$$

$$V_{cos_diff} = V_{COS_P} - V_{COS_N} \quad (4)$$

The offset of the differential signals is the average of the maximum and minimum voltages of the sine or cosine signals. Use [方程式 5](#) and [方程式 6](#) to calculate the offsets for the sine and cosine signals.

$$V_{offset_sin} = \frac{V_{sin_diff(max)} + V_{sin_diff(min)}}{2} \quad (5)$$

$$V_{\text{offset_cos}} = \frac{V_{\text{cos_diff(max)}} + V_{\text{cos_diff(min)}}}{2} \quad (6)$$

For single-ended signals, the offset is the common-mode voltage (V_{CM}).

Use [方程式 7](#) to calculate the differential offset for sine and cosine channels at any given temperature, T_A

$$V_{\text{offset}} = V_{\text{offset, room}} + V_{\text{offset_TC}} \times (T_A - 25^\circ\text{C}) \quad (7)$$

where

- $V_{\text{Offset_TC}}$ is the temperature drift coefficient of the offset
- $V_{\text{Offset_room}}$ is the room temperature offset

Use [方程式 8](#) and [方程式 9](#) to calculate the amplitudes of the differential signals.

$$A_{\text{sin_diff}} = \frac{V_{\text{sin_diff(max)}} - V_{\text{sin_diff(min)}}}{2} \quad (8)$$

$$A_{\text{cos_diff}} = \frac{V_{\text{cos_diff(max)}} - V_{\text{cos_diff(min)}}}{2} \quad (9)$$

Use [方程式 10](#) to calculate the amplitude for single-ended signals.

$$A_{\text{sin_p}} = \frac{V_{\text{sin_p(max)}} - V_{\text{sin_p(min)}}}{2} \quad (10)$$

Amplitude asynchronism refers to the amplitude mismatch error between sine and cosine channels. Use [方程式 11](#) to calculate the amplitude mismatch error.

$$k = 1 - \left(\frac{A_{\text{cos_diff}}}{A_{\text{sin_diff}}} \right) \quad (11)$$

The sine and cosine output signals are typically out-of-phase by 90 degrees, but if an internal phase error occurs owing to sensor and other on chip circuitry non-idealities, the sine and cosine outputs from the sensor can be different than the ideal 90 degrees. This error is referred to as the orthogonality error. This error is defined as the angle error between the zero crossing of the cosine output and maximum value of the sine outputs.

The hysteresis error (ANG_{hyst}) refers to the largest angle error difference between a clockwise rotation and a counter-clockwise rotation.

For the AMR sensor, the orthogonality error and the hysteresis errors are negligible.

6.3.4.2 Transient Parameters

Propagation delay (t_{del_amr}) is defined as the time taken for signal to propagate from magnetic input change to the sine and cosine AMR outputs. The bandwidth limitation of the internal signal conditioning amplifiers causes a phase shift on the applied magnetic field. The propagation delay increases based on the speed of the rotating magnetic field and is specified at the maximum speed of the recommended magnetic field. 图 6-8 shows an input rotating magnetic field and the response of the AMR outputs. The propagation delay leads in the signal path leads to a phase error.

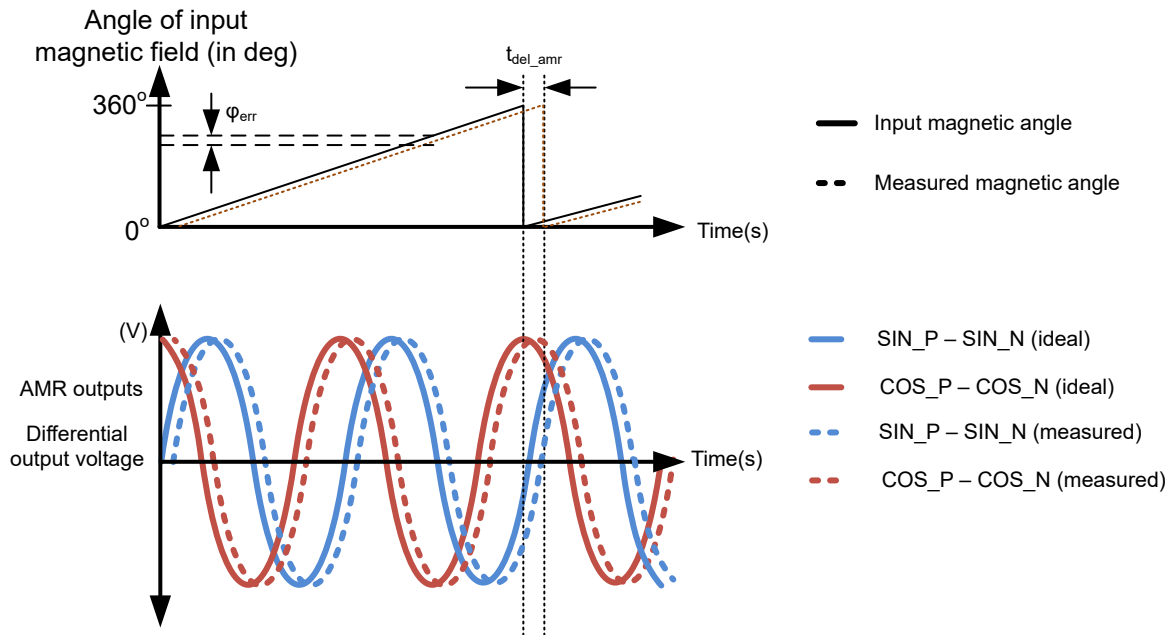


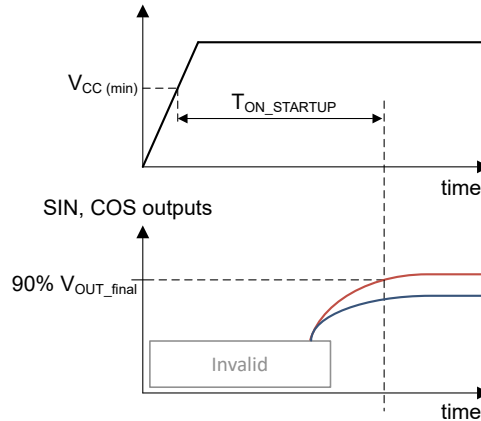
图 6-8. AMR Output Propagation Delay and Phase Error

The phase error (ϕ_{err}) refers to the angle error between the input magnetic field and output of the sensor. This error increases with the speed of the rotating magnetic field and the propagation delay of the AMR sensor. Typically this error can be compensated to the first order if the speed of the rotating magnetic field is known.

6.3.4.2.1 Power-On Time

The power-on time during start-up ($T_{on_startup}$) is defined as the time for the AMR outputs to reach to 90% of the final value (under a constant magnetic field) after the V_{CC} reaches $V_{CC(min)}$. 图 6-9 shows the power-on time of the device when the \overline{SLEEP} pin is tied to V_{CC} during a V_{CC} ramp.

图 6-9. Power-On Time During Start-Up



The power-on time from sleep mode (T_{on_sleep}) is defined as the time for the AMR outputs to reach to 90% of the final value (under a constant magnetic field) after the \overline{SLEEP} reaches above V_{IH_SLEEP} . 图 6-10 shows the power-on time of the device when the \overline{SLEEP} pin is ramped high when the V_{CC} is held constant.

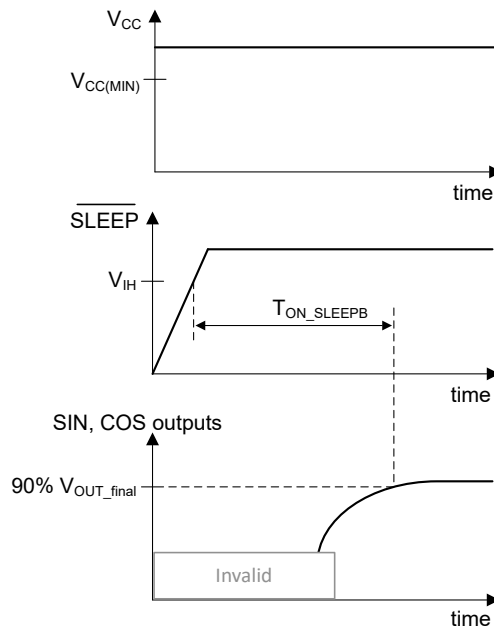


图 6-10. Power-On Time When \overline{SLEEP} is Pulled High

6.3.4.3 Hall Sensor Parameters

The Hall sensors X and Y have factory-calibrated operating (B_{OP}) and release points (B_{RP}). The operating and release points shown in 图 6-4 give the magnetic hysteresis for each Hall sensor.

Use 方程式 12 and 方程式 13 to calculate the symmetry point for each axis.

$$B_{SYM(X)} = B_{OP(X)} + B_{RP(X)} \quad (12)$$

where

- $B_{OP(X)}$ and $B_{RP(X)}$ represent the operating and release points for X Hall sensor

$$B_{SYM(Y)} = B_{OP(Y)} + B_{RP(Y)} \quad (13)$$

where

- $B_{OP(Y)}$ and $B_{RP(Y)}$ represent the operating and release points for Y Hall sensor

Use 方程式 14 to calculate the operating point symmetry.

$$B_{SYM_OP} = B_{OP(X)} - B_{OP(Y)} \quad (14)$$

Use 方程式 15 to calculate the release point symmetry.

$$B_{SYM_RP} = B_{RP(X)} - B_{RP(Y)} \quad (15)$$

6.3.4.4 Angle Accuracy Parameters

The overall angle error represents the relative angular error. 图 6-11 shows the deviation from the reference line after zero angle definition.

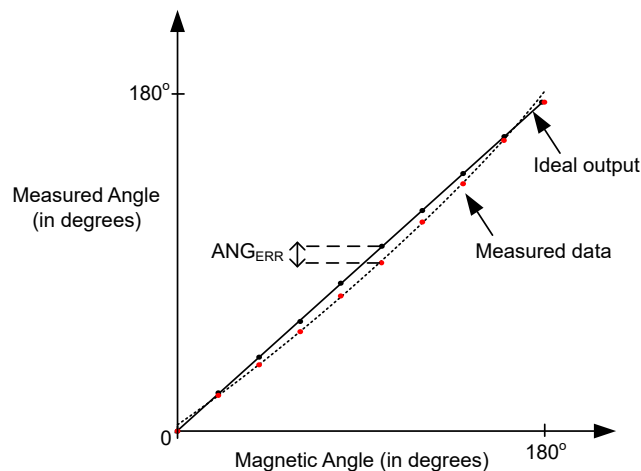


图 6-11. Angle Error

The uncalibrated angular error ($ANG_{ERR_NOCAL_DE}$) is defined as the maximum deviation from an ideal angle without any offset and amplitude mismatch calibration for the VSIN and VCOS differential signals. For single-ended signals, the uncalibrated angular error is denoted by $ANG_{ERR_NOCAL_SE}$.

The single point calibration angular error ($ANG_{ERR_RTCAL_DE}$) is defined as the maximum deviation from an ideal angle after the offset calibration is applied to the VSIN and VCOS differential signals at room temperature (25°C). For single-ended signals, the room temperature offset calibrated angular error is denoted by $ANG_{ERR_RTCAL_SE}$.

The dynamic angular error (ANG_{ERR_DYN}) is defined as the maximum deviation from an ideal angle with the continuous offset and gain calibration applied to the VSIN and VCOS differential signals. The error is measured at 1krpm and includes the phase error owing to the propagation delay of the AMR outputs.

6.3.5 Automatic Gain Control (AGC)

To reduce the drift of the AMR sensor outputs across temperature, the TMAG6181-Q1 features an automatic gain control circuitry where the device changes the gain of the output drivers to keep the final output within an appropriate voltage range on SIN_P, SIN_N, COS_P and COS_N. The AGC block uses the square root of the sum of the squared amplitudes of the two channels to sense amplitude of output signals and set gain selection. This means that the AGC block sets the gain for sine and cosine channels such that the peak-to-peak amplitude of single-ended voltages (V_{OUT}) is within the range listed in [Specifications](#). The AGC block changes the gain of both the sine and cosine channels simultaneously and does not affect the angle accuracy.

If the outputs are out of the intended operating range, the AGC block changes the gain of the sine and cosine channels by a step size of $\pm 1\% V_{CC}$ at an interval of t_{agc_update} , approximately one second, as defined in [Specifications](#). [图 6-12](#) shows the differential AMR outputs for a continuously rotating input field. The shaded area represents the 'No AGC Control' band that represents $\pm 5\%$ of V_{CC} and is centered at 60% of V_{CC} . Notice that the AGC loop reduces the gain of the sine and cosine channels and updates the amplitude of the sine and cosine signals when drift outside of the shaded region at a step size of $1\% V_{CC}$. If the outputs remain within the shaded region, then no action is taken by the AGC control loop.

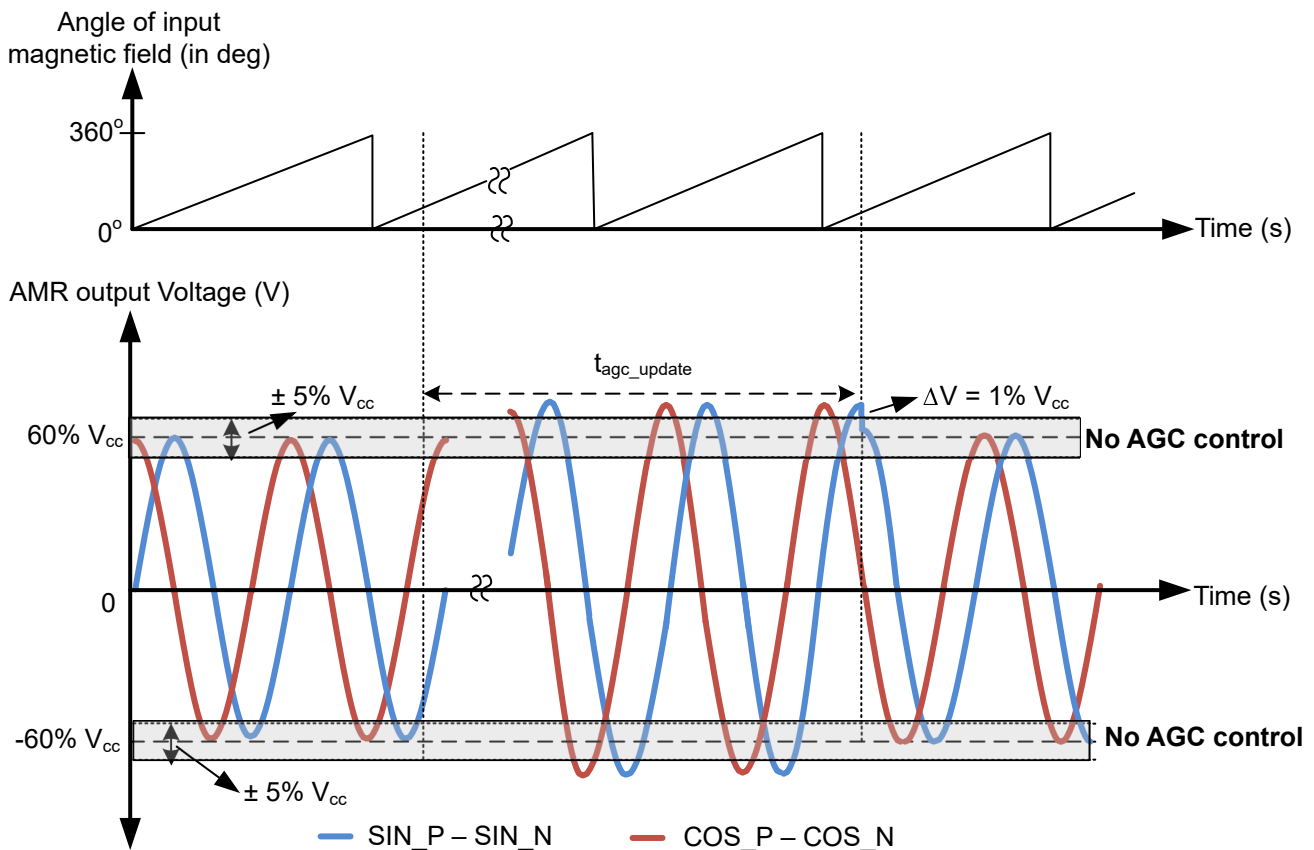


图 6-12. Timing Diagram Showing the Operation of Automatic Gain Control

6.3.6 Turns Counter

The TMAG6181-Q1 features an integrated 11-bit turns counter that can be used to keep track of rotation counts in different modes of operation (see [Device Functional Modes](#)). 图 7-2 shows the typical application diagram when the turns counter is used in the system. The turns counter can be initiated and reset using the open-drain TURNS pin.

The turns counter uses the integrated X and Y Hall sensors to detect the rotation. The outputs from the Hall sensors are sampled at an interval of t_{tc_update} to update the turns counter. The turns counter can detect a change in the applied magnetic field at a resolution of 90° with a range of 360° . The turns counter also keeps track of direction information. The counter is incremented if the applied field is rotated clockwise and decremented if the field is rotated counter-clockwise. 图 6-13 shows the counter operation based on the rotation of the input magnetic field.

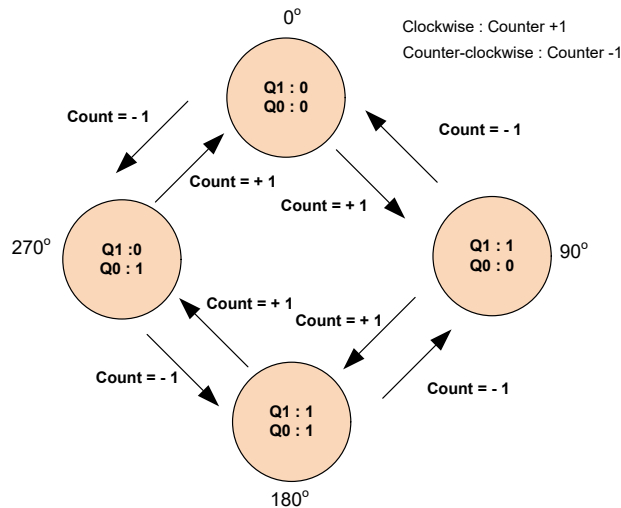


图 6-13. Turns Counter Operation

The turns counter information is sent using the TURNS pin in a Pulse Width Modulation (PWM) format. 图 6-14 shows the PWM duty cycle variation based on the turns counter value. The typical pulse-width modulation (PWM) carrier frequency is 2.5kHz. When the counter value is 0, the TURNS pin outputs a 50% duty cycle.

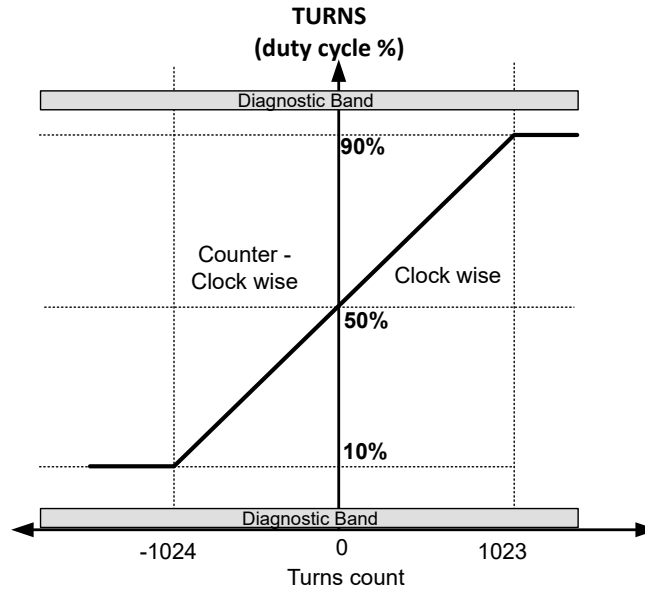


图 6-14. Turns Counter PWM Output on TURNS Pin

图 6-15 shows the timing diagram to enable the turns counter in active mode. When the part powers up, the turns counter is not enabled by default. The turns counter can be enabled by holding the TURNS pin low, for at least t_{tc_start} . When the TURNS pin is released, the pin turns into the output mode and sends out the PWM pulses corresponding to the internal turns counter after a time delay of t_{tc_delay} , as provided in the [Specifications](#). To reset the counter and disable the output on TURNS pin, TURNS is pulled low for at least $t > t_{tc_reset}$. When the turns counter is enabled in the active mode, the internal wake-up oscillator is used to enable the Hall sensor signal path at regular intervals to update the turns counter information. This allows the device to keep the turns counter feature on at a lower power consumption overhead.

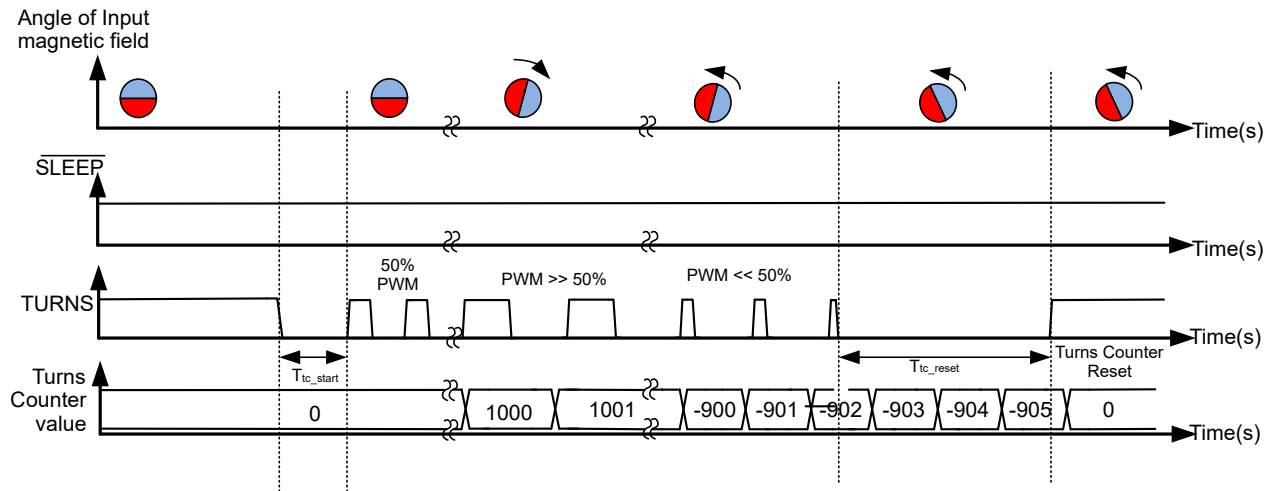


图 6-15. Timing Diagram Showing the Turns Counter Operation in Active Mode

图 6-16 shows the timing diagram of the turns counter operation in low-power mode. The part can be placed in a low-power, duty-cycled state if the turns counter is enabled. During this low-power state, an internal wake-up oscillator is used to wake up the device at regular intervals, t_{sleep} . When the part wakes up, the integrated Hall sensor signals are monitored for a rotation, and the turns counter information is updated. The TURNS pin does not output the PWM pulses during this low-power state. When the part is moved into an active state from the low-power state, the PWM pulses corresponding to the turns counter information saved during the low-power state is sent out to the microcontroller.

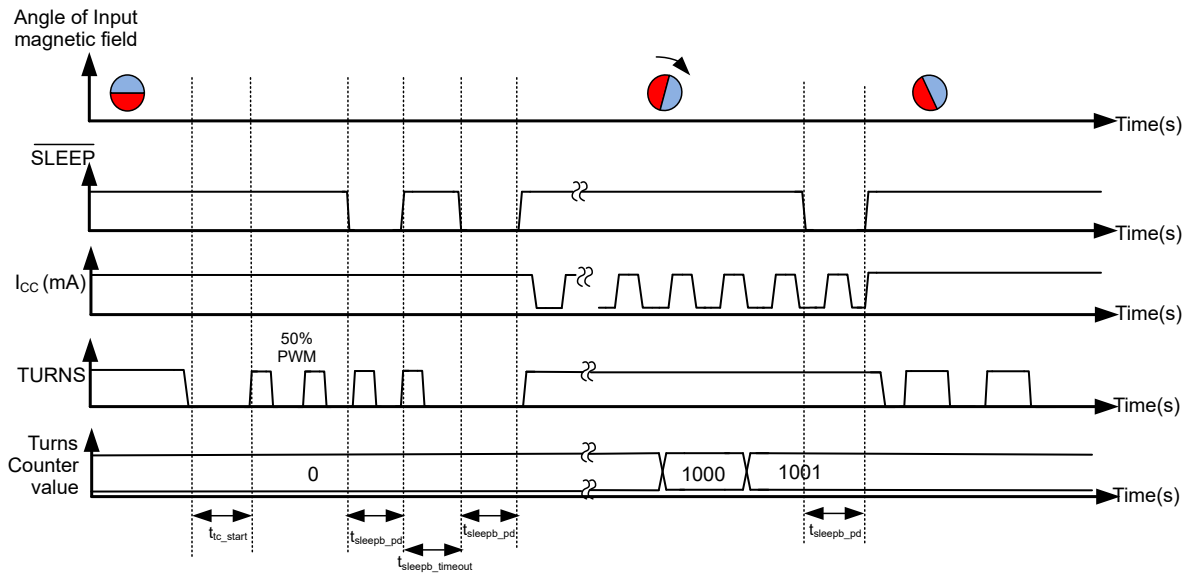


图 6-16. Timing Diagram Showing the Turns Counter Operation in Low-Power Mode

6.3.6.1 Rotation Tracking

The TMAG6181-Q1 has a rotation tracking feature to track high-speed rotations at low current consumption to save power. This feature lets the device decrease the sleep time when magnetic field rotations are detected, and enables the device to track the higher speed and acceleration events. When the turns counter is enabled using the TURNS pin, the rotation tracking feature is also enabled. This feature is enabled in both the active-turns mode and the low-power mode (see [Device Functional Modes](#)).

In active mode, when the turns counter is enabled, the Hall sensor signals are monitored approximately every 1.6ms. But when a rotation is detected, the period between the next wake-up event is reduced by 1/8th to 0.2ms. If no rotation change is detected for four consecutive periods, then the sleep time is increased to 0.4ms. If new rotations are detected, then the tracking algorithm goes to 0.2ms and the counter for the four consecutive periods is reset. If no rotations are detected in next four consecutive periods, then the sleep time is doubled again to 0.8ms and then eventually back to 1.6ms if no more rotations are detected. After reaching this default value of 1.6ms, the rotation tracking feature allows the TMAG6181-Q1 to continue sampling at this fixed period until new rotations are detected.

In low-power mode, when the turns counter is enabled, the Hall sensor signals are monitored approximately every 25.6ms. But when a rotation is detected, the period between the next wake-up event is reduced by 1/16th to 1.6ms. If no rotation change is detected for four consecutive periods, then the sleep time is increased to 3.2ms. If new rotations are detected, then the tracking algorithm goes to 1.6ms and the counter for the four consecutive periods is reset. If no rotations are detected in the next four consecutive periods, then the sleep time is doubled again to 6.4ms and then to 12.8ms after the next four cycles. After reaching the default value of 25.6ms, the rotation tracking feature allows the TMAG6181-Q1 to continue sampling at this fixed period until new rotations are detected. 图 6-17 shows the rotation tracking feature during low-power mode.

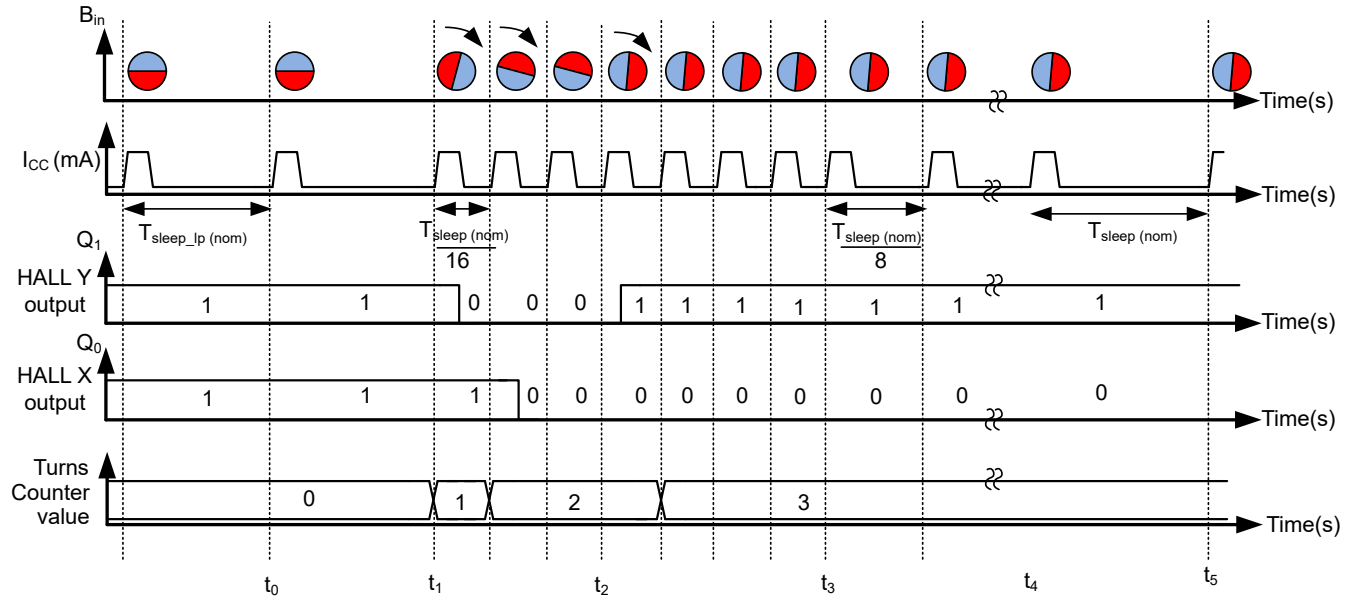


图 6-17. Timing Diagram Showing the Rotation Tracking Feature in Low Power Mode

Use 方程式 16 to calculate the maximum angle travel, θ , for a rotating magnetic field.

$$\theta = 6[(v \times t) + (0.5 \times a \times t^2)] \quad (16)$$

where,

- t is the field travel time
- v is the velocity of the moving field in rpm
- a is the acceleration of the moving field in rpm/second

The turns counter has an angle resolution of 90°. The turns counter can successfully track the rotations if the counter ensures the state transitions (as shown in 图 6-13) and does not jump any states. At an acceleration of 6000rpm/sec, the rotation tracking feature enables the device to track up to 8000rpm during low-power mode and in active-turns mode, and enables at track up to 60000rpm.

表 6-1 shows the trade-off between maximum speed (in rpm) that can be tracked by the turns counter and the current consumption in active mode.

表 6-1. Maximum Trackable rpm vs Average Current Consumption in Active Mode

OPERATING MODE	SLEEP TIME (ms)	MAX TRACKABLE rpm (TYP)	AVERAGE CURRENT (μ A)
Active-turns mode	0.2	62500	472
	0.4	31250	280
	0.8	15625	167
	1.6 (default)	7812	105

表 6-2 shows the trade-off between maximum speed (in rpm) that can be tracked by the turns counter and the current consumption in low-power mode

表 6-2. Maximum Trackable rpm vs Average Current Consumption in Low Power Mode

OPERATING MODE	SLEEP TIME (ms)	MAX TRACKABLE rpm (TYP)	AVERAGE CURRENT (ILP) (μ A)
Low-power mode	1.6	7812	105
	3.2	3905	73
	6.4	1950	57
	12.8	970	48
	25.6 (default)	475	44

6.3.7 Safety and Diagnostics

The TMAG6181-Q1 supports several device and system level diagnostics features to detect, monitor, and report failures during the device operation.

In the event of a failure, the TMAG6181-Q1 is placed in a FAULT state, where the outputs from the AMR sensors are placed in a high-impedance state. See [Device Functional Modes](#) for fault state transition from different operation modes. As shown in the [Application and Implementation](#) section, users can add pullup or pulldown resistors on SIN_P, SIN_N, COS_P, and COS_N pins at the termination site (that is the microcontroller). The resistors are generally pulled up to supply voltage or pulled down to ground such that the ADC code on MCU is out of expected range. This state signal faults to the microcontroller.

The integrated turns counter has a valid range of 10% to 90% PWM output. If a fault is detected in the turns counter, then the output of the turns counter is at >95% PWM or <5% PWM. The external microcontroller can monitor if the turns counter is within expected range.

The TMAG6181-Q1 performs the following device and system level checks:

6.3.7.1 Device Level Checks

- AMR signal path checks
 - AMR sensor bias check
 - AMR output signals common mode check
 - Automatic gain control loop check
- Hall sensor signal path checks
 - Hall sensor bias and resistance check
 - Hall sensor comparator check
- Turns counter overflow check
- Power management and supporting circuitry checks
 - Internal LDO undervoltage check
 - Internal clocks integrity check
- Internal memory integrity check (or a cyclic redundancy check - CRC)

6.3.7.2 System Level Checks

- V_{CC} undervoltage and overvoltage checks
- Pin level opens and short checks

6.4 Device Functional Modes

6.4.1 Operating Modes

The TMAG6181-Q1 supports multiple operating modes for a wide array of applications as explained in [图 6-18](#). The device starts powering up after the V_{CC} supply crosses the minimum threshold as specified in the *Recommended Operating Conditions* table.

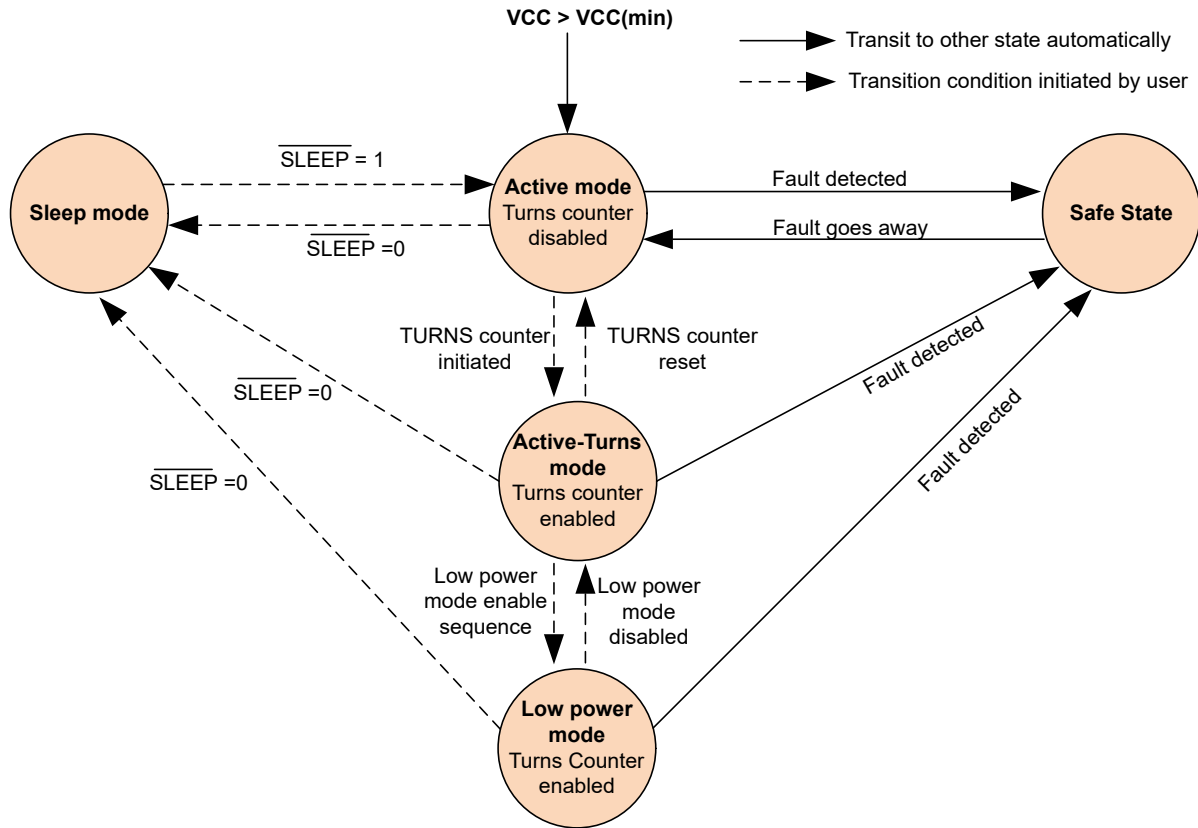


图 6-18. TMAG6181-Q1 State Transition Diagram

表 6-3 shows the different operating modes of the TMAG6181-Q1.

表 6-3. TMAG6181-Q1 Operating Modes

OPERATING MODE	DEVICE FUNCTION	AMR OUTPUT STATE	TURNS PIN (I/O FUNCTIONALITY) ⁽¹⁾
Active mode	AMR outputs track the magnetic field direction	Outputs in normal operating range	I
Active-turns mode	AMR outputs track the magnetic field direction and the TURNS pin sends out the internal turns counter information using PWM	Outputs in normal operating range	I / O
Low-power mode	Wakes up at a certain interval and the turns counter keeps track of changes in X and Y Hall sensor state	High Impedance	N/A
Sleep mode	Device enters the lowest power state	High Impedance	N/A

表 6-3. TMAG6181-Q1 Operating Modes (续)

OPERATING MODE	DEVICE FUNCTION	AMR OUTPUT STATE	TURNS PIN (I/O FUNCTIONALITY) ⁽¹⁾
Fault (safe) mode	Device has detected a fault condition	High Impedance	N/A

(1) I = Input, O = Output, I/O = Input/ Output , N/A = Not Available

6.4.1.1 Active Mode

After power up, if the $\overline{\text{SLEEP}}$ is pulled high, the TMAG6181-Q1 enters active mode where the SIN_P, SIN_N, COS_P, and COS_N outputs actively provide the angle of the applied magnetic field. In this mode, the turns counter is disabled and the TURNS pin does not provide the PWM output. The average current consumption during the active conversion is I_{ACT} .

6.4.1.2 Active-Turns Mode

In this mode, the AMR sensor outputs are active and the turns counter is enabled. In this mode, the TURNS pin acts as a I/O and provides the counter information using the PWM output. In this mode, the Hall sensors are enabled and the outputs are monitored to update the turns counter. To enter this mode, the TURNS pin is pulled low for $t > t_{\text{tc_start}}$. 图 6-15 shows the sequence to enter active-turns mode. After the TURNS pin is released, the falling edge of the first PWM pulse occurs after $t_{\text{tc_start}}$. To exit this mode, the TURNS pin is pulled low for $t > t_{\text{tc_reset}}$ as defined in the [Specifications](#) section. After the turns counter is reset, the part goes back into active mode.

6.4.1.3 Low-Power Mode

The TMAG6181-Q1 can enter into low-power mode after the turns counter is enabled. During this mode, the turns counter is active and the wake-up oscillator is used to wake the device at a regular interval, T_{sleep} . During this mode, the TURNS pin does not send out the PWM information and the input path is disabled. The average current consumption during this mode is denoted by I_{LP} .

图 6-16 shows the sequence to enter and exit low-power mode from active-turns mode. To enter low-power mode, two consecutive pulses are provided on the $\overline{\text{SLEEP}}$ pin. The pulses must be within the range of $t_{\text{sleep_pd}}$ as provided in the [Specifications](#) section. The timeout between these two consecutive pulses is defined by $t_{\text{sleep_timeout}}$ in the [Specifications](#) section. To exit low-power mode, the part monitors for a rising edge on the $\overline{\text{SLEEP}}$ pin. This can be provided by driving the $\overline{\text{SLEEP}}$ at a pulse width identical to the one used to enter low-power mode.

During low-power mode, an internal wake-up oscillator is used to wake the device up at regular intervals to monitor the states of the Hall sensors. During low-power mode, the rotation tracking feature is enabled to track rotations up to 8krpm at a very low power consumption. The period between two consecutive wake-up intervals is dependent on the frequency of the applied magnetic field (see [Rotation Tracking](#)).

6.4.1.4 Sleep Mode

Sleep mode can place the device in the lowest current consumption state. When the voltage on $\overline{\text{SLEEP}}$ pin goes below V_{IL} and stays low for longer than $t_{\text{sleep_mode}}$, then the part enters sleep mode. The average current consumption during this mode is denoted by I_{SLEEP} , and this mode uses approximately ten times less current compared to low-power mode. The part exits sleep mode when the voltage on the $\overline{\text{SLEEP}}$ pin goes above V_{IH} .

There is a 500k Ω pulldown resistor on the $\overline{\text{SLEEP}}$ pin and, when the $\overline{\text{SLEEP}}$ pin left floating, the part enters the sleep mode. TI recommends to ensure the $\overline{\text{SLEEP}}$ pin is driven externally to a known logic state.

6.4.1.5 Fault Mode

The TMAG6181-Q1 supports extensive fault diagnostics as detailed in [Diagnostics](#) section. When a fault is detected, the part enters fault mode. In this mode, the AMR outputs are placed in a high-impedance state.

7 Application and Implementation

备注

Information in the following applications sections is not part of the TI component specification, and TI does not warrant its accuracy or completeness. TI's customers are responsible for determining suitability of components for their purposes, as well as validating and testing their design implementation to confirm system functionality.

7.1 Application Information

7.1.1 Power Supply as the Reference for External ADC

The AMR output signals of the TMAG6181-Q1 are ratiometric to the supply voltage, V_{CC} . This enables the external ADC to use the TMAG6181-Q1 supply voltage as a reference and eliminate the errors that can arise if a separate reference voltage is used. This also enables to optimize the external ADC input range. TI therefore recommends to use the supply voltage (V_{CC}) as the reference for the external ADCs. To ensure the noise on the power supply is minimized, TI recommends using a 0.1 μ F bypass capacitor.

7.1.2 AMR Output Dependence on Airgap Distance

The AMR sensor is only sensitive to the direction of the applied magnetic field along the X-Y plane parallel to the chip surface. The applied magnetic field from a rotating magnet can vary based on the airgap distance between the TMAG6181-Q1 and the magnet.

As long the absolute field magnetic field is above the minimum field listed in *Recommended Operating Conditions*, the angle accuracy from the AMR outputs are independent of the value of the applied magnetic field.

7.1.3 Calibration of Sensor Errors

The TMAG6181-Q1 is factory-calibrated for the best angular accuracy. Some of the electrical errors from the sensor that impact the angle accuracy can be calibrated out for achieving the best performance. 图 7-1 shows the impact of the different sensor error parameters such as offset, amplitude mismatch and orthogonality error on the angle accuracy.

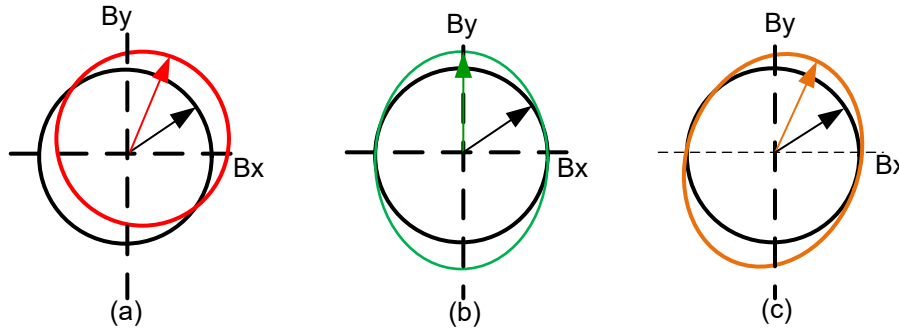


图 7-1. Angle Accuracy Impact Owing to Sensor Electrical Errors (a) Offset Error (b) Amplitude Mismatch Error (c) Orthogonality Error

Based on the parameters defined in [AMR Output Parameters](#), the angle from the AMR sensors is given by 方程式 17:

$$\theta = \frac{\arctan2\left(\frac{A_{\sin} \sin(2\theta) + V_{\text{offset_sin}}}{A_{\cos} \cos(2\theta) + V_{\text{offset_cos}}}\right)}{2} \quad (17)$$

where

- $V_{\text{offset_sin}}$ and $V_{\text{offset_cos}}$ are the differential offsets of the sine and cosine outputs
- A_{\sin} and A_{\cos} are the differential amplitude of the sine and cosine outputs

The impact of the angle accuracy owing to the orthogonality error and the hysteresis errors is negligible for the TMAG6181-Q1 and can be ignored.

To calibrate the offset and amplitude mismatch errors, the magnetic field rotates over the entire range and the sine and cosine outputs are sampled continuously to obtain the minimum and maximum values of the outputs.

Users can calculate the average of the minimum and maximum values of the respective outputs across the full angle range to find the offset error of the sine and cosine outputs. Use 方程式 18 and 方程式 19 to calculate the offset correction parameters for sine and cosine.

$$V_{\text{os_sin_cal}} = \frac{V_{\sin(\text{max})} + V_{\sin(\text{min})}}{2} \quad (18)$$

$$V_{\text{os_cos_cal}} = \frac{V_{\cos(\text{max})} + V_{\cos(\text{min})}}{2} \quad (19)$$

Users can calculate the difference of the minimum and maximum values of the respective outputs across the full angle range to find the amplitude of the sine and cosine outputs. Use 方程式 20 to calculate the amplitude correction parameters for sine and cosine.

$$A_{\text{corr}} = 1 - \frac{V_{\sin(\text{max})} - V_{\sin(\text{min})}}{V_{\cos(\text{max})} - V_{\cos(\text{min})}} \quad (20)$$

7.2 Typical Application

The TMAG6181-Q1 AMR angle sensor can be used in either in single-ended output mode or differential output mode. TMAG6181-Q1 has the drive capability to either drive differential-ended or single-ended SAR or Sigma Delta ADCs. Typically, an external microcontroller processes the AMR output signals to extract the angular position.

The differential-ended output mode is helpful to eliminate any common mode disturbances in the system. 图 7-2 shows a typical application circuit where the differential output signals SIN_P, SIN_N, COS_P, and COS_N are all connected to the four single-ended ADCs in the external microcontroller. If differential ADCs are available, then they are typically recommended. The load capacitors and resistors must match each other to typically achieve high accuracy. During sleep mode or when a fault is detected, the outputs are placed in high-impedance state. To ensure that external microcontroller can detect this case, TI recommends using pulldown or pullup resistors.

The TMAG6181-Q1 can drive capacitive loads up to 10nF directly on the AMR output pins and, for a cable with capacitances of 100pF/m, the device can drive up to 100m capacitive loads. With the ability to source and sink currents up to 1mA, the device can drive resistive loads.

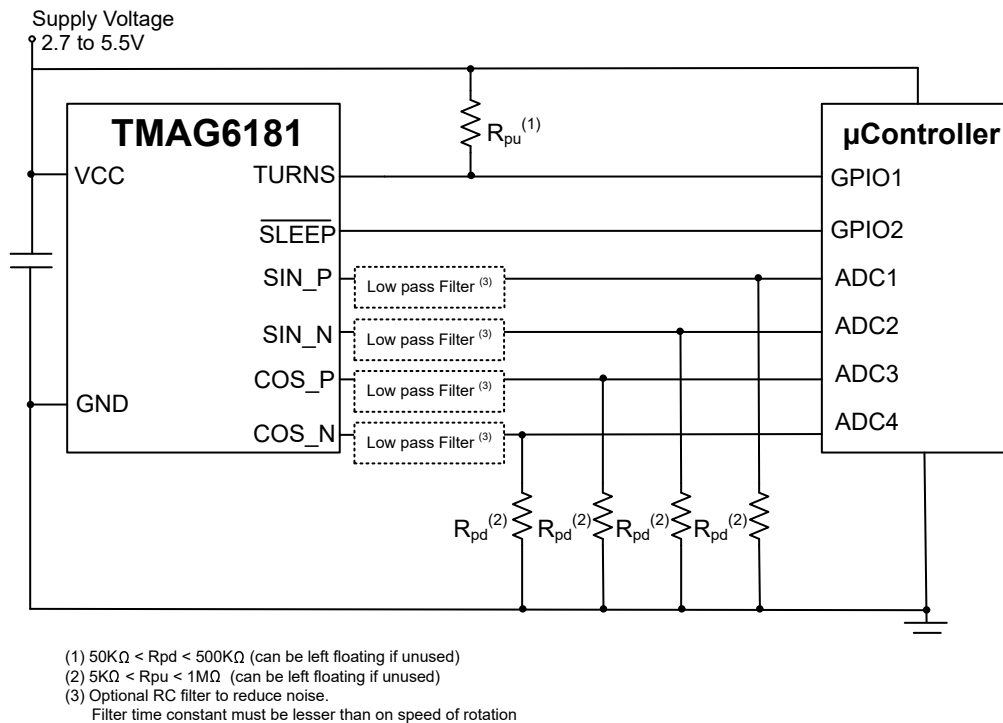
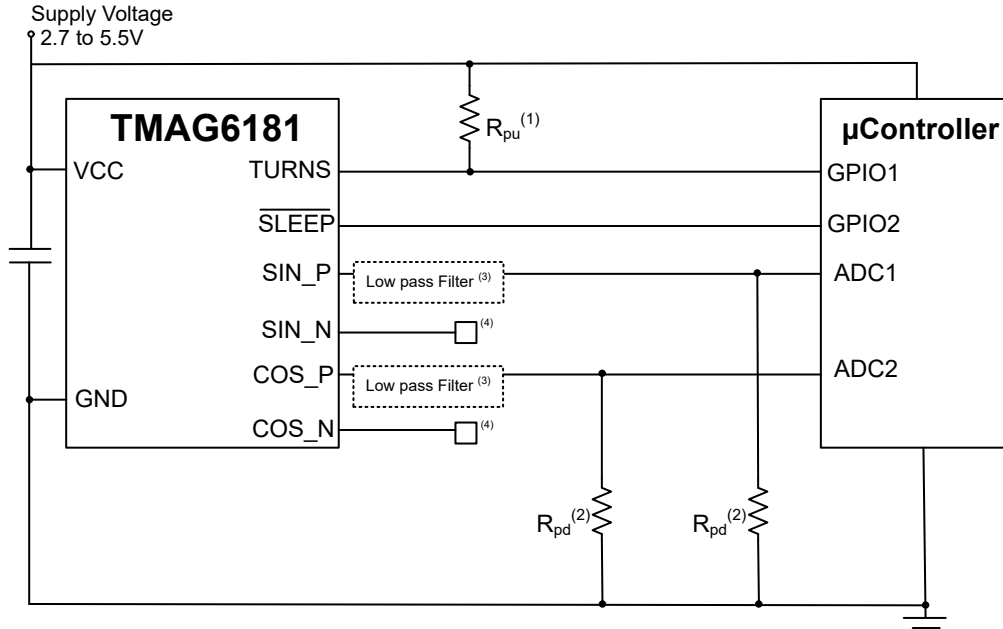


图 7-2. Application Diagram for TMAG6181-Q1 in Differential-Ended Output Mode

If the number of ADC ports in the microcontroller are limited, or if the number of wires from the sensor to the microcontroller must be kept to a minimum, 图 7-3 shows a typical application circuit where only the positive output channels (SIN_P and COS_P) are connected to single-ended ADCs. The unused output signals (SIN_N and COS_N) can be either left floating or connected to ground through a high resistance. In single-ended output mode, the dynamic range (SNR) and noise immunity is typically reduced compared to the differential output mode. To reduce noise on the outputs and for filtering EMC disturbances, an external low-pass filter such as a first order RC network can be used. The bandwidth of the external filter must be designed based on the rotation speed of the magnetic field to be detected. TI recommends adding pullup or pulldown resistors to ground on the single-ended outputs (SIN_P and COS_P) so the outputs are defined when the outputs are in high-impedance state. The supply voltage of the sensor is used as the reference for the ADCs in the microcontroller.



- (1) $50\text{K}\Omega < R_{pu} < 500\text{K}\Omega$ (can be left floating if unused)
- (2) $5\text{K}\Omega < R_{pd} < 1\text{M}\Omega$ (can be left floating if unused)
- (3) Optional RC filter to reduce noise.
Filter time constant must be lesser than on speed of rotation
- (4) Can be left floating or connected to ground through $R > 100\text{K}\Omega$

图 7-3. Application Diagram for TMAG6181-Q1 in Single-Ended Output Mode

7.2.1 Design Requirements

图 7-4 shows the center of the magnet aligned with the center of the sensor in a typical on-axis application.

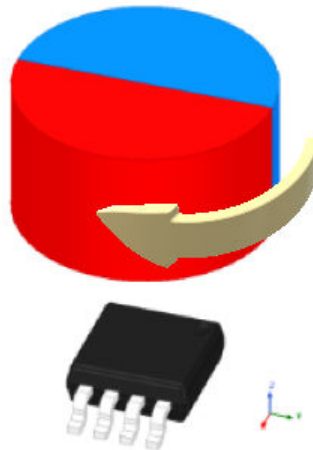


图 7-4. On-Axis Measurement Setup for TMAG6181-Q1

Use the parameters listed in 表 7-1 for this design example.

表 7-1. Design Parameters

DESIGN PARAMETERS	ON-AXIS MEASUREMENT
V_{CC}	5V
Magnet	Cylinder: 4.7625mm diameter, 12.7mm thick, neodymium N52, Br = 1480
Output mode	Differential-ended
Maximum speed of the motor	8,000rpm
Desired angle error across temperature	<1°
Magnet to sensor placement	End of shaft

7.2.2 Detailed Design Procedure

For accurate angle measurement, the center of the magnet is aligned to the center of the sensor with acceptable tolerances. Follow these steps to ensure that the sensor is calibrated for best accuracy:

- Reference angle calibration - Set the reference angle based on the magnet alignment to the sensor. This error can be saved in the microcontroller for runtime absolute position calculation. This error is also known as angle offset in a system.
- Electrical offset calibration - See [Calibration of Sensor Errors](#) for the offset calibration procedure. If the sensor cannot be rotated across the full range, then the electrical offsets cannot be calibrated.
- Amplitude mismatch calibration - See [Calibration of Sensor Errors](#) for the amplitude mismatch calibration procedure. If the sensor cannot be rotated across the full range, then the amplitude mismatch cannot be calibrated.

7.2.2.1 Designing with Multiple Sensors

Some applications have the need for multiple angle position sensors to either detect position in different parts of the system or for redundancy.

7.2.2.1.1 Designing for Redundancy

For applications that require the highest level of functional safety, two angle sensors can be required for redundancy purposes.

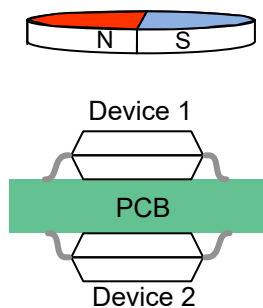


图 7-5. Two Sensors Placed on Either Side of the Board for Redundancy

To achieve redundancy without any impact on the angle accuracy, the TMAG6181-Q1 devices can be placed on either side of the PCB as shown in 图 7-5. The AMR sensors are sensitive to only the direction of the magnetic field and independent on the value of the absolute magnetic field, therefore the magnetic accuracy is not compromised even if the devices are placed on the other side of the PCB.

7.2.2.1.2 Multiplexing Multiple Sensors

Some applications require multiple angle position sensors to detect position in different parts of the system. In those cases, the primary challenge would be the availability of multiple ADC that are required to digitize the information from the sensors. In cases where the sensor is placed remotely away from the microcontroller, there can be multiple output lines between the sensor and microcontroller.

With the ability to place the output in high-impedance state during shutdown mode, multiple TMAG6181-Q1 devices can share the analog output. This can minimize the system cost by using a single ADC per channel. 图 7-6 shows two devices that share the same analog output, with their respective SLEEP pins controlled by the microcontroller. A pull-down resistor can be used to pull the output to ground when both the devices are placed in shutdown mode.

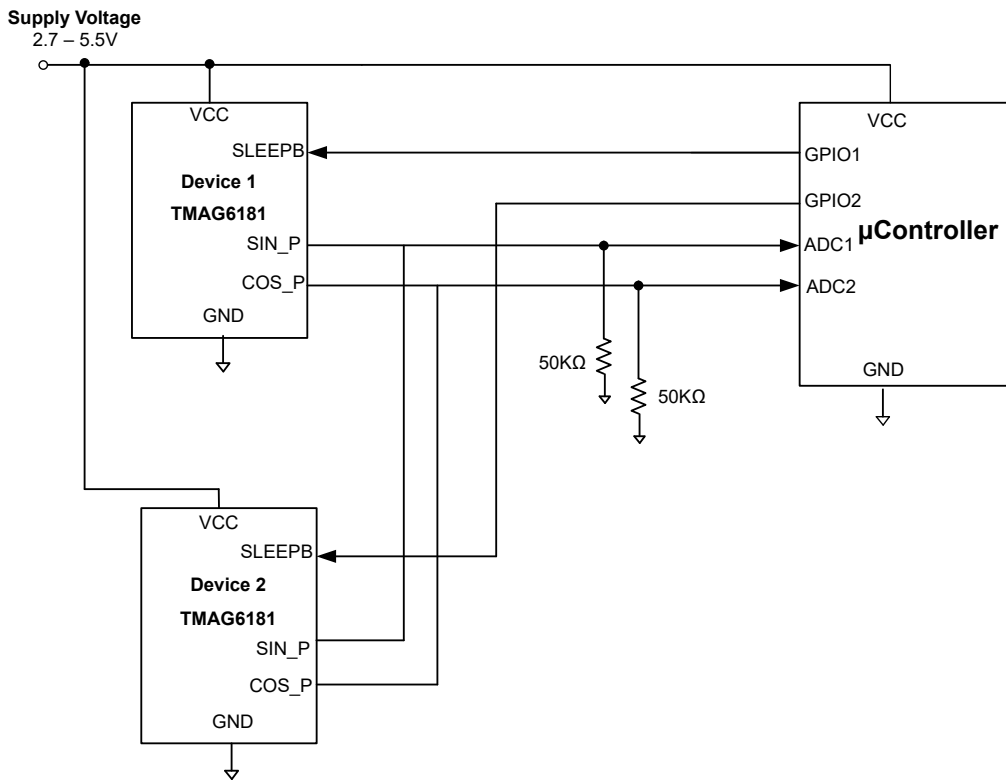


图 7-6. Multiple Sensors with Shared Output

图 7-7 shows how the GPIOs of the microcontroller can be used to multiplex the outputs from the two sensors. When the GPIO1 goes high, Device 1 is enabled and drives the output line to the corresponding output after the power-on time. During this time, GPIO2 is driven low and Device 2 is placed in shutdown mode. When the output from the second device must be measured, the first device must be turned off before the second device is enabled, indicated by t_{mux} in the timing diagram. B_1 and B_2 correspond to the magnetic fields seen by Device 1 and Device 2 respectively.

With the ability to support up to 10nF capacitive loads, the TMAG6181-Q1 can connect multiple sensors to the same output.

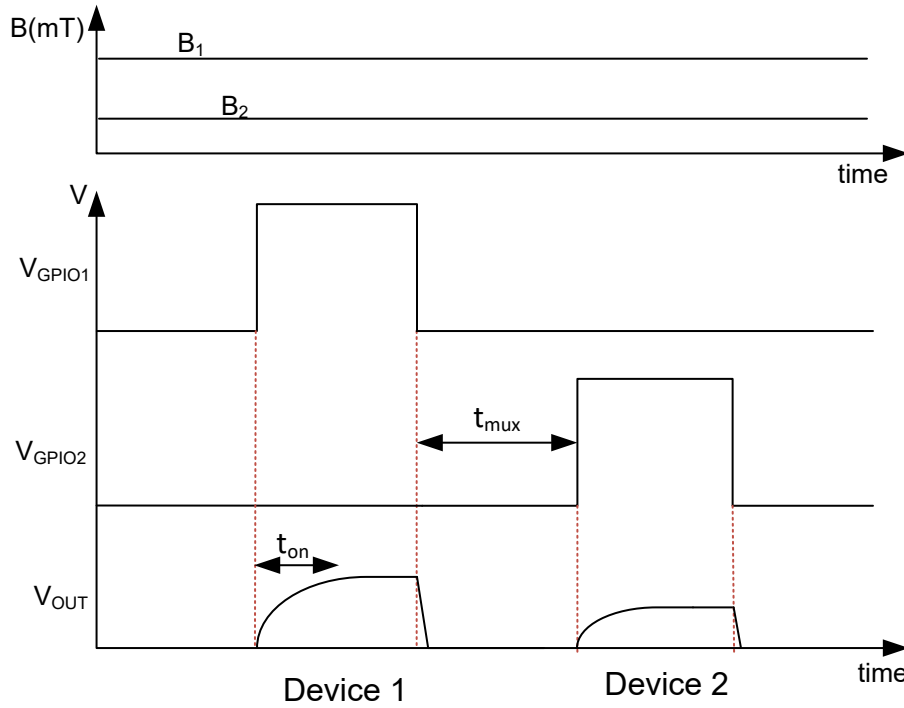


图 7-7. Timing Diagram for Multiplexing the Sensor Outputs

7.2.3 Application Curve

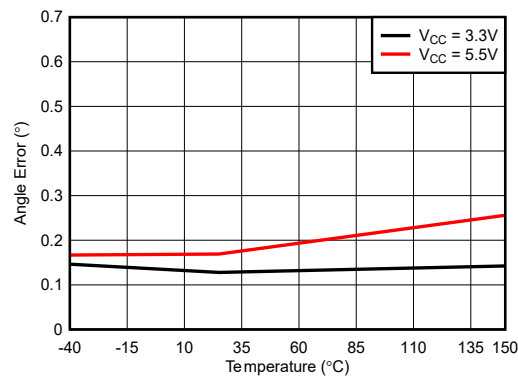


图 7-8. Angle Error with Dynamic Calibration

7.3 Power Supply Recommendations

A decoupling capacitor close to the device must be used to provide local energy with minimal inductance. TI recommends using a ceramic capacitor with a value of at least 0.01 μ F.

7.4 Layout

7.4.1 Layout Guidelines

Magnetic fields pass through most nonferromagnetic materials with no significant disturbance. Embedding magnetic sensors within plastic or aluminum enclosures and sensing magnets on the outside is common practice. Magnetic fields also easily pass through most printed circuit boards (PCBs), which makes placing the magnet on the opposite side of the PCB possible.

7.4.2 Layout Example

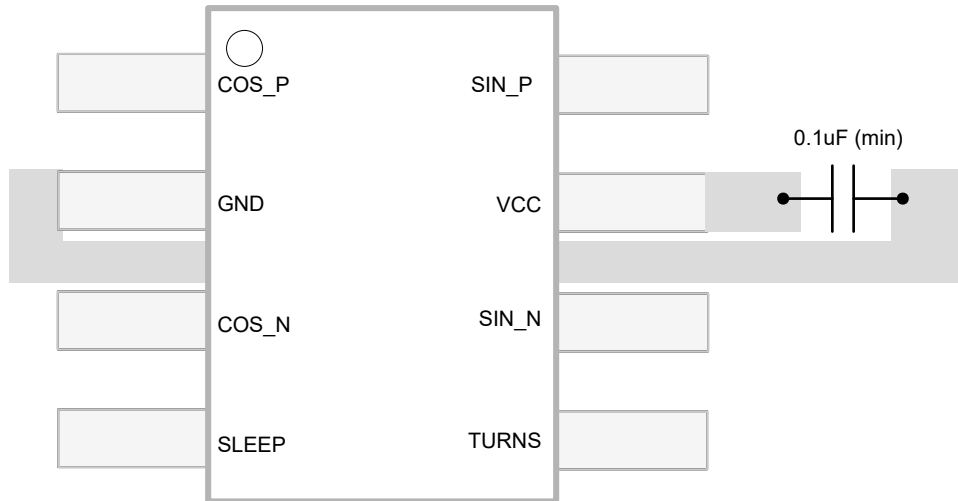


图 7-9. Layout Example With TMAG6181-Q1

8 Device and Documentation Support

8.1 接收文档更新通知

要接收文档更新通知，请导航至 ti.com 上的器件产品文件夹。点击 [通知](#) 进行注册，即可每周接收产品信息更改摘要。有关更改的详细信息，请查看任何已修订文档中包含的修订历史记录。

8.2 支持资源

[TI E2E™ 中文支持论坛](#) 是工程师的重要参考资料，可直接从专家处获得快速、经过验证的解答和设计帮助。搜索现有解答或提出自己的问题，获得所需的快速设计帮助。

链接的内容由各个贡献者“按原样”提供。这些内容并不构成 TI 技术规范，并且不一定反映 TI 的观点；请参阅 TI 的 [使用条款](#)。

8.3 Trademarks

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ESD 的损坏小至导致微小的性能降级，大至整个器件故障。精密的集成电路可能更容易受到损坏，这是因为非常细微的参数更改都可能会导致器件与其发布的规格不相符。

8.5 术语表

[TI 术语表](#) 本术语表列出并解释了术语、首字母缩略词和定义。

9 Revision History

Changes from Revision A (February 2024) to Revision B (August 2024) Page

- Added $V_{\text{offset_room}}$ and $t_{\text{agc_update}}$ table notes to the *Electrical Characteristics* table.....5
- Removed $V_{\text{CC_RAMP}}$ from the Electrical Characteristics table..... 5

Changes from Revision * (March 2023) to Revision A (February 2024) Page

- 将数据表状态从“预告信息”更改为：“量产数据” 1

10 Mechanical, Packaging, and Orderable Information

The following pages include mechanical, packaging, and orderable information. This information is the most current data available for the designated devices. This data is subject to change without notice and revision of this document. For browser-based versions of this data sheet, refer to the left-hand navigation.

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
TMAG6181EDGKRQ1	ACTIVE	VSSOP	DGK	8	2500	RoHS & Green	SN	Level-1-260C-UNLIM	-40 to 150	6181	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSELETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead finish/Ball material - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

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TAPE AND REEL INFORMATION

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
TMAG6181EDGKRQ1	VSSOP	DGK	8	2500	330.0	12.4	5.25	3.35	1.25	8.0	12.0	Q1

TAPE AND REEL BOX DIMENSIONS


*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
TMAG6181EDGKRQ1	VSSOP	DGK	8	2500	366.0	364.0	50.0

DGK0008A



PACKAGE OUTLINE

VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE



4214862/A 04/2023

NOTES:

PowerPAD is a trademark of Texas Instruments.

1. All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
2. This drawing is subject to change without notice.
3. This dimension does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.15 mm per side.
4. This dimension does not include interlead flash. Interlead flash shall not exceed 0.25 mm per side.
5. Reference JEDEC registration MO-187.

EXAMPLE BOARD LAYOUT

DGK0008A

™ VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE



LAND PATTERN EXAMPLE
EXPOSED METAL SHOWN
SCALE: 15X



SOLDER MASK DETAILS

4214862/A 04/2023

NOTES: (continued)

6. Publication IPC-7351 may have alternate designs.
7. Solder mask tolerances between and around signal pads can vary based on board fabrication site.
8. Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.
9. Size of metal pad may vary due to creepage requirement.

EXAMPLE STENCIL DESIGN

DGK0008A

TM VSSOP - 1.1 mm max height

SMALL OUTLINE PACKAGE



SOLDER PASTE EXAMPLE
SCALE: 15X

4214862/A 04/2023

NOTES: (continued)

11. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.
12. Board assembly site may have different recommendations for stencil design.

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