

DLP® Products Technical Advisory

1 Initialization Issue With DLPC200 (DLPR200F)

Reference Documents:

- [DLPC200](#) – DLP® Digital Controller for the DLP5500 DMD
- [DLPZ004](#) – DLP® 0.55 XGA Chipset Data Sheet ([DLP5500](#), [DLPC200](#), [DLPA200](#))

1.1 Technical Advisory Description

Several customers reported and TI was able to reproduce a DLPC200 initialization failure, when using the DLPR200F firmware, that occurred in certain rare instances for specific combinations of temperature and 1.2-V power supply variations (within specification 1.15 to 1.25 V).

1.2 TI Action

TI created and tested a new DLPR200F firmware build to verify that this initialization problem was corrected.

2 Recommended Customer Action

TI strongly recommends that all users of the DLP5500 chipset update their DLPR200F firmware code to the latest version set (HW v455 – MCU v2.2.0) by downloading the bin file from TI at [DLPR200F](#). Select the download button for DLPR200F.

For any questions, please contact DLP® Products:

[TI E2E Community](#)

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