



12500 TI Boulevard, MS 8640, Dallas, Texas 75243

**PCN# 20080201001  
Datasheet Errata for TS3A5018  
Information Only**

Dear Customer:

This is an information-only announcement of a change to a device that is currently offered by Texas Instruments.

The changes discussed within this PCN are for your information only. Please see the attachment details for the planned implementation date.

This notification period is per TI's standard process. Any negotiated alternative change requirements will be provided via the customer's defined process. Customers with previously negotiated, special requirements will be handled separately. Any inquiries should be directed to your local Field Sales Representative.

For questions regarding this notice, contact your local Field Sales Representative or the PCN Manager ([PCN\\_ww\\_admin\\_team@list.ti.com](mailto:PCN_ww_admin_team@list.ti.com)).

Sincerely,

PCN Team  
SC Business Services  
Phone: +1(214) 480-6037  
Fax: +1(214) 480-6659

<b>PCN Number:</b>	20080201001	<b>PCN Date:</b>	02/29/2008
<b>Title:</b>	Datasheet Errata for TS3A5018		
<b>Customer Contact:</b>	<a href="#">Linda K Miles</a>	<b>Phone:</b>	903-868-7638
<b>Dept:</b>	Standard Linear and Logic		
<b>Proposed 1<sup>st</sup> Ship Date:</b>	N/A	<b>Estimated Sample Availability:</b>	N / A
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process
<input type="checkbox"/>	Design	<input checked="" type="checkbox"/>	Electrical Specification
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Assembly Materials
<input type="checkbox"/>		<input type="checkbox"/>	Mechanical Specification
<input type="checkbox"/>		<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

## PCN Details

### Description of Change:

Texas Instruments Standard Linear and Logic is issuing this notification because the datasheet does not reflect the correct performance of the device. There is no change to silicon. Changes are highlighted in yellow below.

#### FEATURES Before:

- Low ON-State Resistance (10  $\Omega$ )
- Low Charge Injection
- Excellent ON-State Resistance Matching
- Low Total Harmonic Distortion (THD)
- 2.3-V to 3.6-V Single-Supply Operation
- **Control Inputs Are 5.5-V Tolerant**
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Performance Tested Per JESD 22
  - 2000-V Human-Body Model (A114-B, Class II)
  - 1000-V Charged-Device Model (C101)

#### FEATURES After:

- Low ON-State Resistance (10  $\Omega$ )
- Low Charge Injection
- Excellent ON-State Resistance Matching
- Low Total Harmonic Distortion (THD)
- 2.3-V to 3.6-V Single-Supply Operation
- Latch-Up Performance Exceeds 100 mA Per JESD 78, Class II
- ESD Performance Tested Per JESD 22
  - 2000-V Human-Body Model (A114-B, Class II)
  - 1000-V Charged-Device Model (C101)

#### Absolute Minimum and Maximum Ratings Before:

	MIN	MAX	UNIT
$V_{NC}$			
$V_{NO}$ Analog voltage range(3)(4)	-0.5	7	V
$V_{COM}$			
$V_I$ Digital input voltage range(3)(4)	-0.5	7	V

#### Absolute Minimum and Maximum Ratings After:

	MIN	MAX	UNIT
$V_{NC}$			
$V_{NO}$ Analog voltage range(3)(4)	-0.5	<b>4.6</b>	V
$V_{COM}$			
$V_I$ Digital input voltage range(3)(4)	-0.5	<b>4.6</b>	V

**Parameter Before:****Electrical Characteristics for 3.3-V Supply**

Digital Control Inputs (IN, /EN)(2)								
Parameter	Symbol	Test Conditions	TA	V+	MIN	TYP	MAX	UNIT
Input leakage current	$I_{IH}, I_{IL}$	V = 5.5 V or 0V	25°C	3.6 V	-1	0.05	1	$\mu\text{A}$
			Full		-1		1	

**Parameter After:****Electrical Characteristics for 3.3-V Supply**

Digital Control Inputs (IN, /EN)(2)								
Parameter	Symbol	Test Conditions	TA	V+	MIN	TYP	MAX	UNIT
Input leakage current	$I_{IH}, I_{IL}$	V = <b>V+</b> or 0V	25°C	3.6 V	-1	0.05	1	$\mu\text{A}$
			Full		-1		1	

**Parameter Before:****Electrical Characteristics for 2.5-V Supply**

Digital Control Inputs (IN, /EN)(2)								
Parameter	Symbol	Test Conditions	TA	V+	MIN	TYP	MAX	UNIT
Input leakage current	$I_{IH}, I_{IL}$	V = 5.5 V or 0V	25°C	2.7 V	-0.1	0.05	-0.1	$\mu\text{A}$
			Full		-1		1	

**Parameter After:**

Digital Control Inputs (IN, /EN)(2)								
Parameter	Symbol	Test Conditions	TA	V+	MIN	TYP	MAX	UNIT
Input leakage current	$I_{IH}, I_{IL}$	V = <b>V+</b> or 0V	25°C	2.7 V	-0.1	0.05	0.1	$\mu\text{A}$
			Full		-1		1	

**Literature Number**

Device Type	Current Literature #	New Literature #
TS3A5018	SCDS189A	SCDS189B

**Reason for Change:**

This change is being made to correct the Datasheet.

**Anticipated impact on Fit, Form, Function & Reliability (positive / negative):**

Texas Instruments does not anticipate a negative impact on fit, form, function or reliability.

**Changes to product identification resulting from this PCN:**

There is not change to product identification.

**Product Affected:**

TS3A5018DBQRE4	TS3A5018DRE4	TS3A5018PWR
TS3A5018DBQRG4	TS3A5018DRG4	TS3A5018PWRE4
TS3A5018DE4	TS3A5018PW	TS3A5018PWRG4
TS3A5018DG4	TS3A5018PWE4	TS3A5018RGYR
TS3A5018DGVRE4	TS3A5018PWG4	TS3A5018RGYRG4
TS3A5018DGVRG4		

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
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Interface	<a href="http://interface.ti.com">interface.ti.com</a>
Logic	<a href="http://logic.ti.com">logic.ti.com</a>
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Security	<a href="http://www.ti.com/security">www.ti.com/security</a>
Telephony	<a href="http://www.ti.com/telephony">www.ti.com/telephony</a>
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