

The Stackup Legend below this is static.
If you change the stackup, update the Legend.

Layer Name	Layer Document	Copper Thickness	Dielectric Material
Top Solder Mask	(.GTS)		Solder Resist
Top Layer	(.GTL)	1.4mil	FR-4
Bottom Layer	(.GBL)	1.4mil	FR-4
Bottom Solder Mask	(.GBS)		Solder Resist

DESIGN INFORMATION

MIN. TRACK WIDTH: 8 MIL
 MIN. CLEARANCE: 0.2 mm
 MIN. VIA PAD SIZE: 24 MIL
 MINIMUM ANNULAR RING 0.05mm (2ML) EXTERNAL
 PER IPC-D-275 CLASS 2 LEVEL C
 REGISTRATION TOLERANCES: METAL +/- 5 MIL, HOLES +/- 3 MIL

MATERIAL:
 FR-408 FR-4 High Tg OTHER _____
 THICKNESS: 62 MIL (1.6mm) +/-10% OTHER _____
 TOLERANCE: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____
 BOW & TWIST: ANSI IPC-6012 TYPE 3 CLASS 2
 OTHER +/- _____

DRILLING:
 REFERENCE: AS SHOWN NC_DRILL FILES
 PTH MIN COPPER THICKNESS: 1MIL OTHER _____

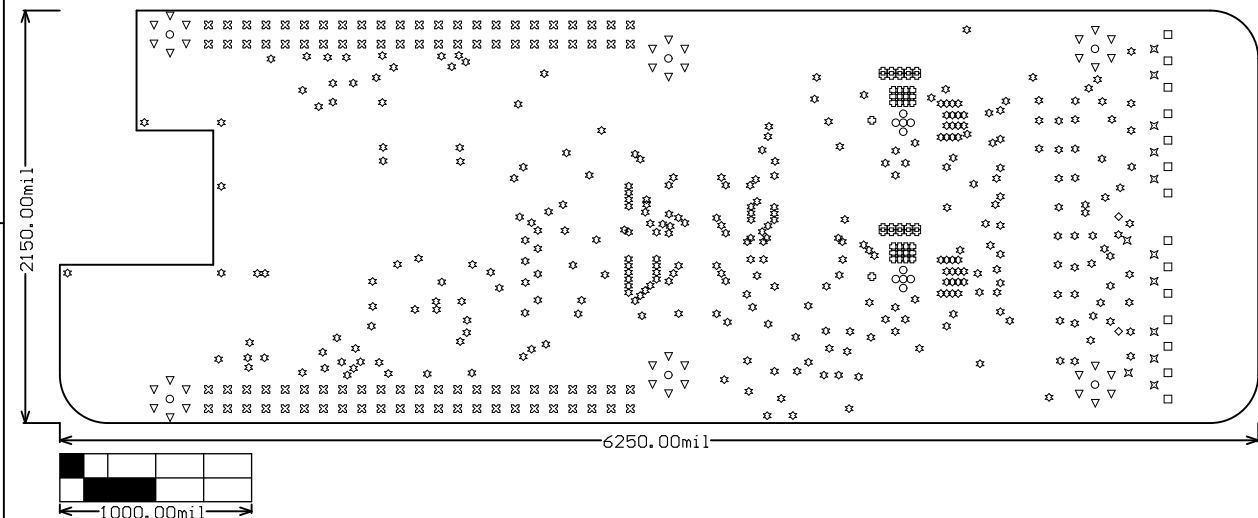
BOARD FINISH:
 SILKSCREEN: TOP BOTTOM
 SILKSCREEN COLOR: WHITE OTHER _____
 SOLDER RESIST COLOR:
 GREEN BLUE OTHER _____

SURFACE FINISH: IMMERSION GOLD (ENIG) ENEPIG
 IMM. TIN/SILVER OR EQUIV OTHER _____

ARRAY/PANEL: CUT AND TRIM PER MECH LAYER 1
 N.C. ROUTE V. SCORE

CERTIFICATION: MATERIALS AND WORKMANSHIP FOR ALL PCBs TO MEET OR EXCEED THE REQUIREMENTS OF:
 ANSI IPC-A-600F CLASS -> 1 2 3
 UL 94V-0 RoHS OTHER PER ORDER

ADDITIONAL REQUIREMENTS:
 MICROSECTION: YES
 BARE BOARD ELEC. TEST: NONE REQUIRED PER ORDER
 MANUFACTURER'S UL: RAIL METAL SILK



Symbol	Hit Count	Tool Size	Plated	Hole Type
○	10	7.874mil (0.2mm)	PTH	Round
☆	46	8mil (0.203mm)	PTH	Round
✱	328	12mil (0.305mm)	PTH	Round
▽	36	19.685mil (0.5mm)	PTH	Round
⊗	12	28mil (0.711mm)	PTH	Round
⊗	92	40.157mil (1.02mm)	PTH	Round
◇	2	43.307mil (1.1mm)	PTH	Round
□	14	47.244mil (1.2mm)	PTH	Round
○	6	137.795mil (3.5mm)	PTH	Round
546 Total				

Drill Table

ALL ARTWORK VIEWED FROM TOP SIDE	BOARD #: TIDA-00320	REV: E1	SUN REV: Not In VersionControl	Texas Instruments (TI) and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. TI and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. TI and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.	ENGINEER: Ingolf Frank	LAYOUT BY: Ingolf Frank
LAYER NAME = Fabrication Drawings	GENERATED : 12/15/2014 5:06:46 PM	TEXAS INSTRUMENTS			SCALE: 1.00	ALTIM DESIGNER VERSION: 14.3.15.35511
PLOT NAME = Fabrication Drawing						



PROJECT TITLE:
BeagleBone Cape 8ch 0.5A Low Side for PLC

DESIGNED FOR:
Public Release

FILE NAME:
TIDA-00320_BeagleBone_Cape_8ch_LS.PcbDoc

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