

Revision History				
Rev	ECN #	Approved Date	Approved by	Notes
N/A	N/A	N/A	N/A	N/A

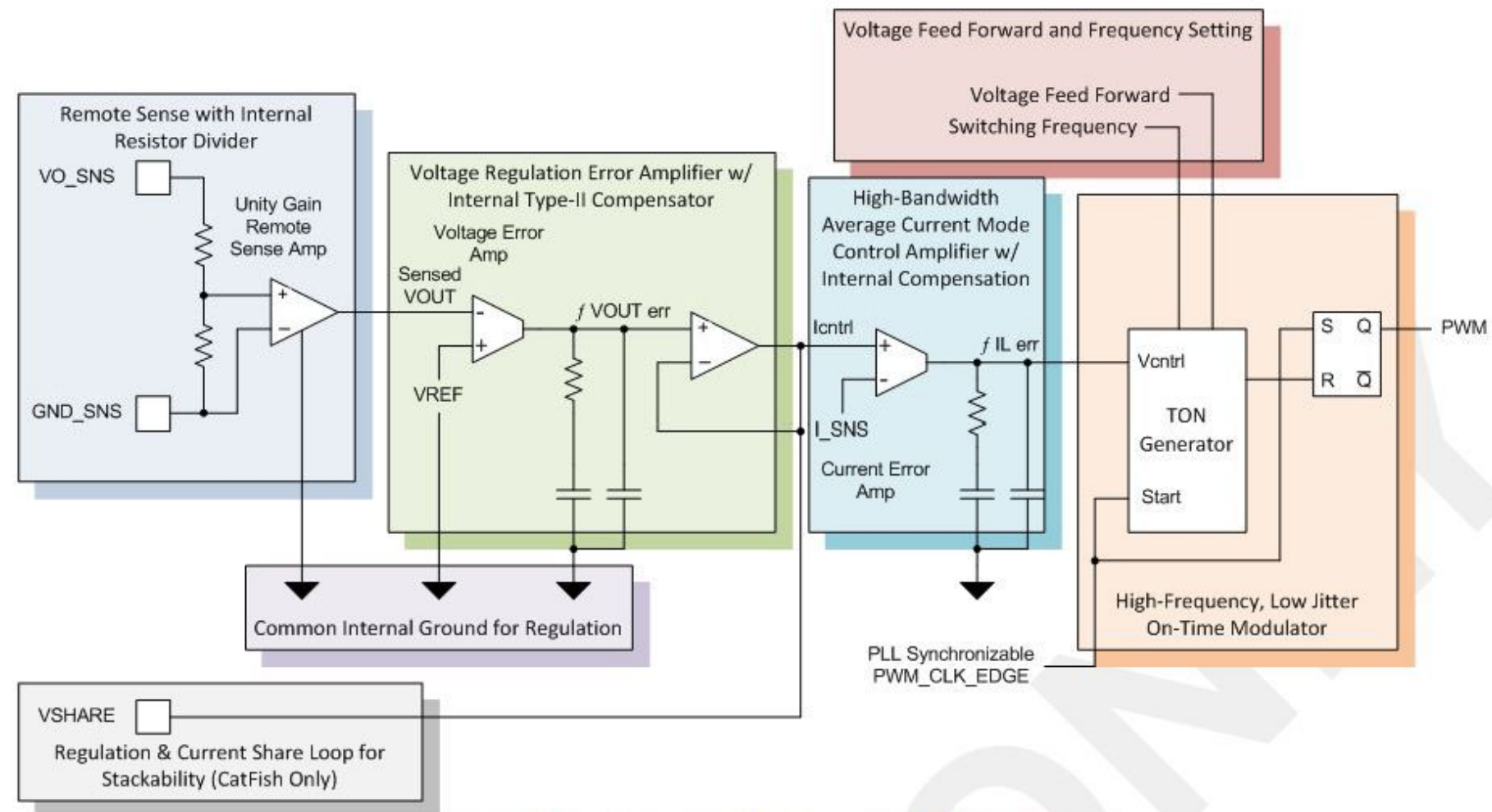

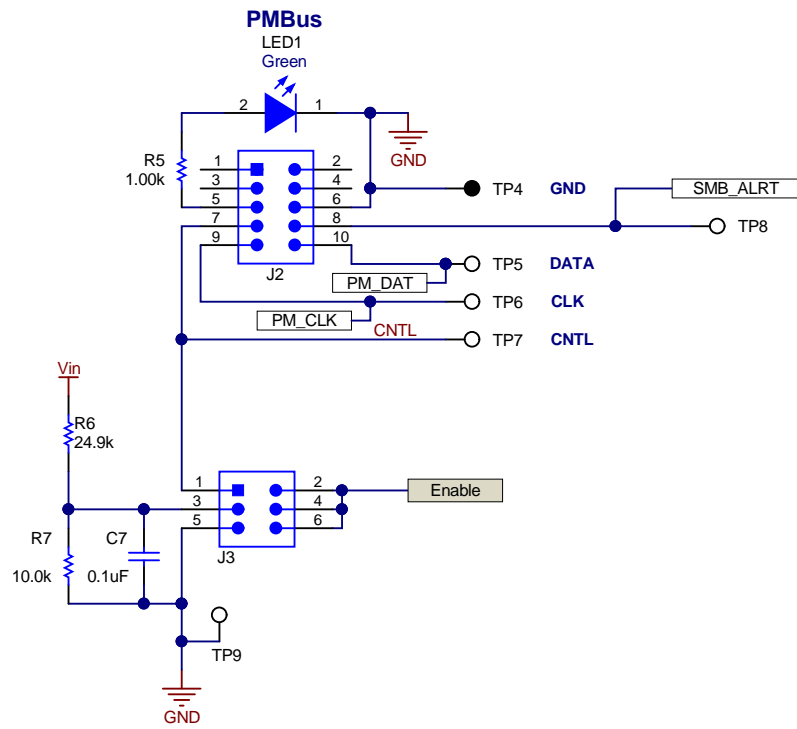


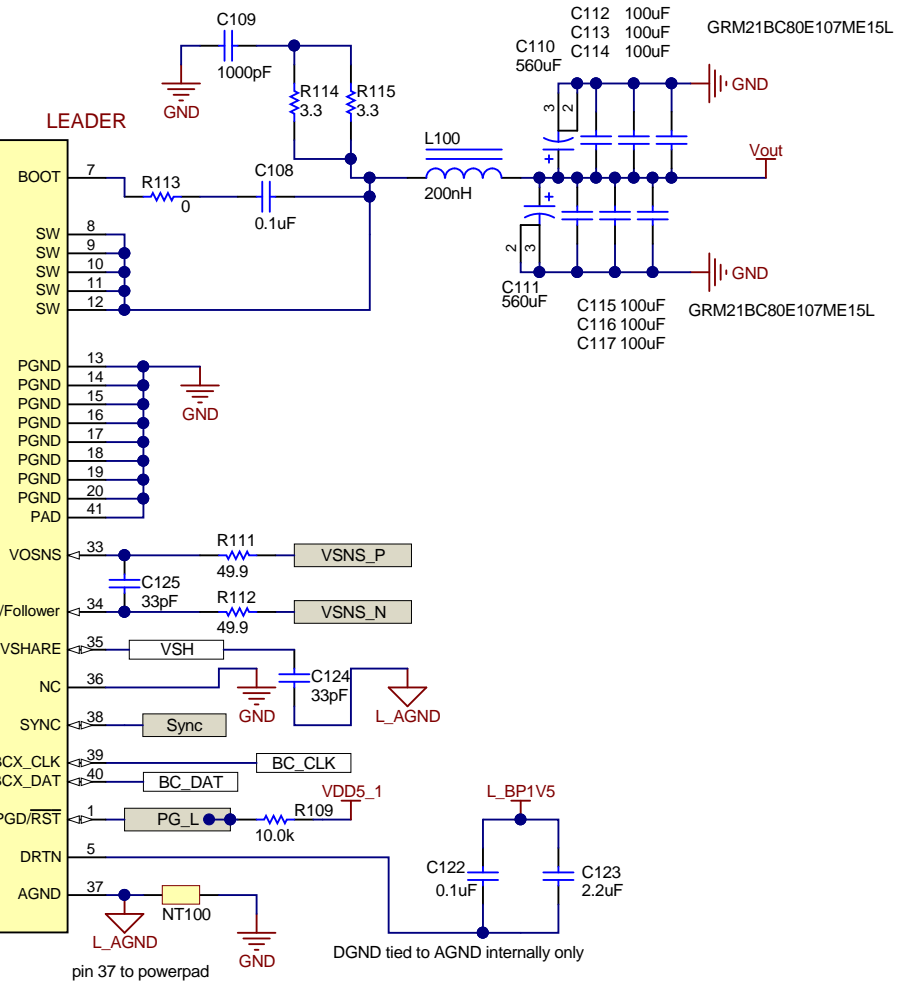
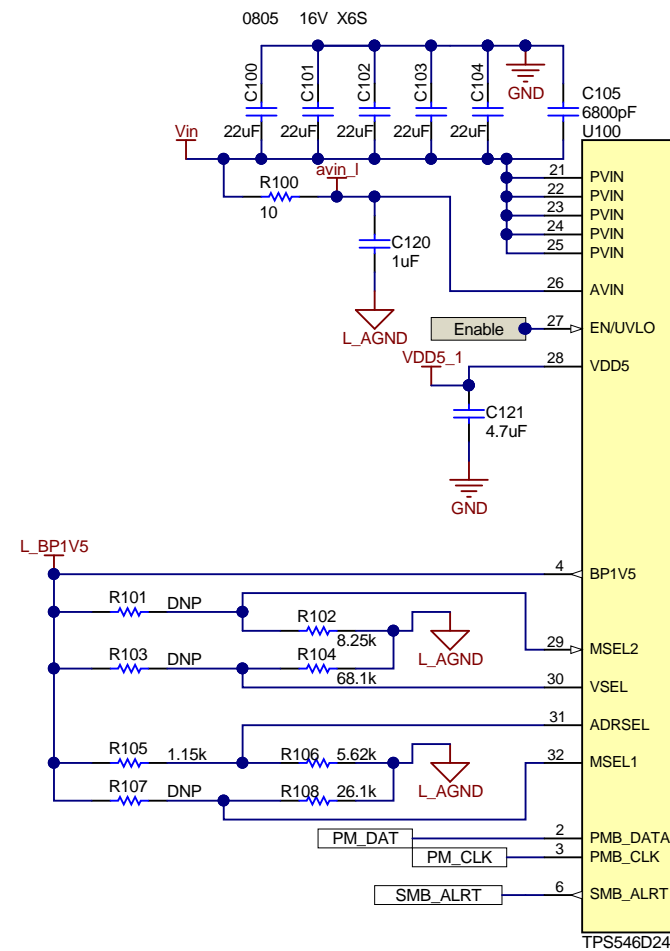
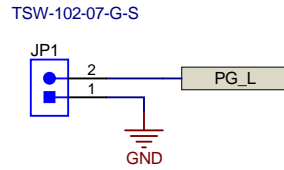
Figure 3: Average Current Mode Control Block Diagram

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Orderable: no	Designed for: Public Release	Mod. Date: 1/6/2021	 http://www.ti.com © Texas Instruments 2021
TID #: N/A	Project Title: TPS546D24A 4 Phases Compact Design	Sheet: 1 of 3	
Number: PMP21254	Rev: B	Size: B	
SVN Rev: Not in version control	Assembly Variant: [No Variations]		
Drawn By: Josh Mandelcorn	File: PMP21254B_CoverSheet.SchDoc	Contact: http://www.ti.com/support	

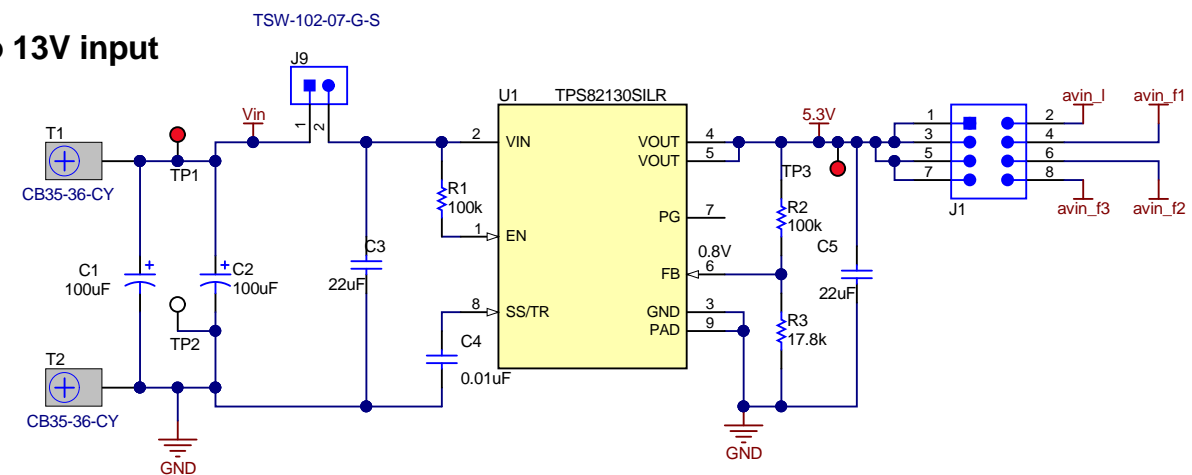


VIN_ON set to 3.84V. VIN_OFF set to 3.0V.

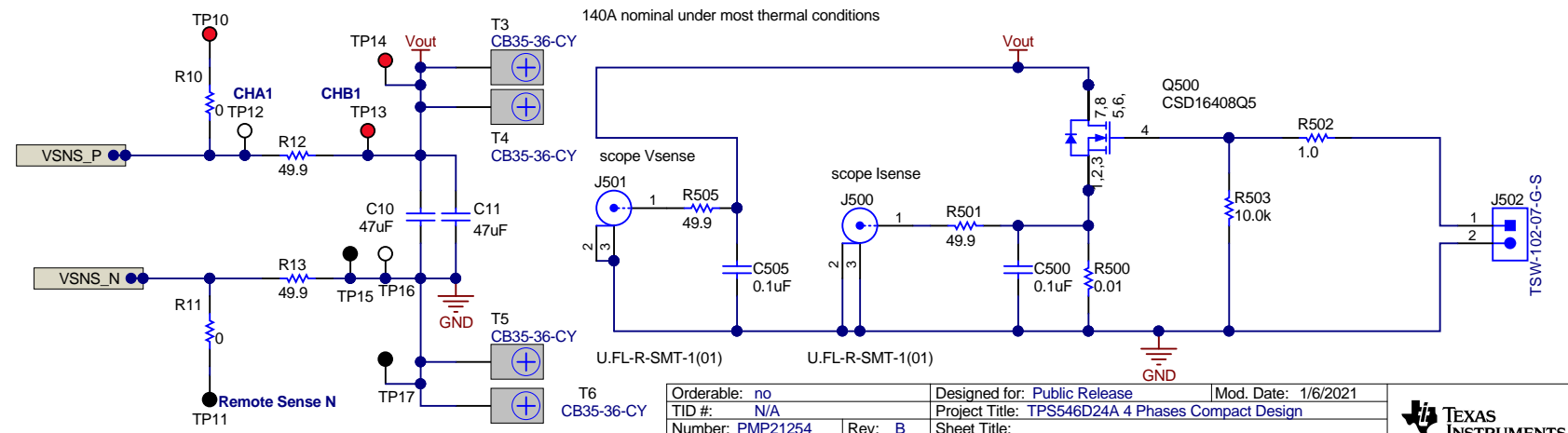


- 3 options for powering bias:
- 1) From main Power VIN: populate R100, R200, R300, R400 OMIT J9 & J1
 - 2) Use U1 regulator off main Power VIN: Populate J9 & J1, OMIT R100, R200, R300, R400
 - 3) Separate AVIN: Use TP3 and a ground for this aux power. Populate J1, OMIT J9, R100, R200, R300, R400

9V to 13V input



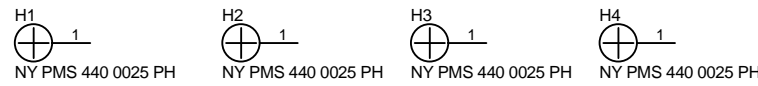
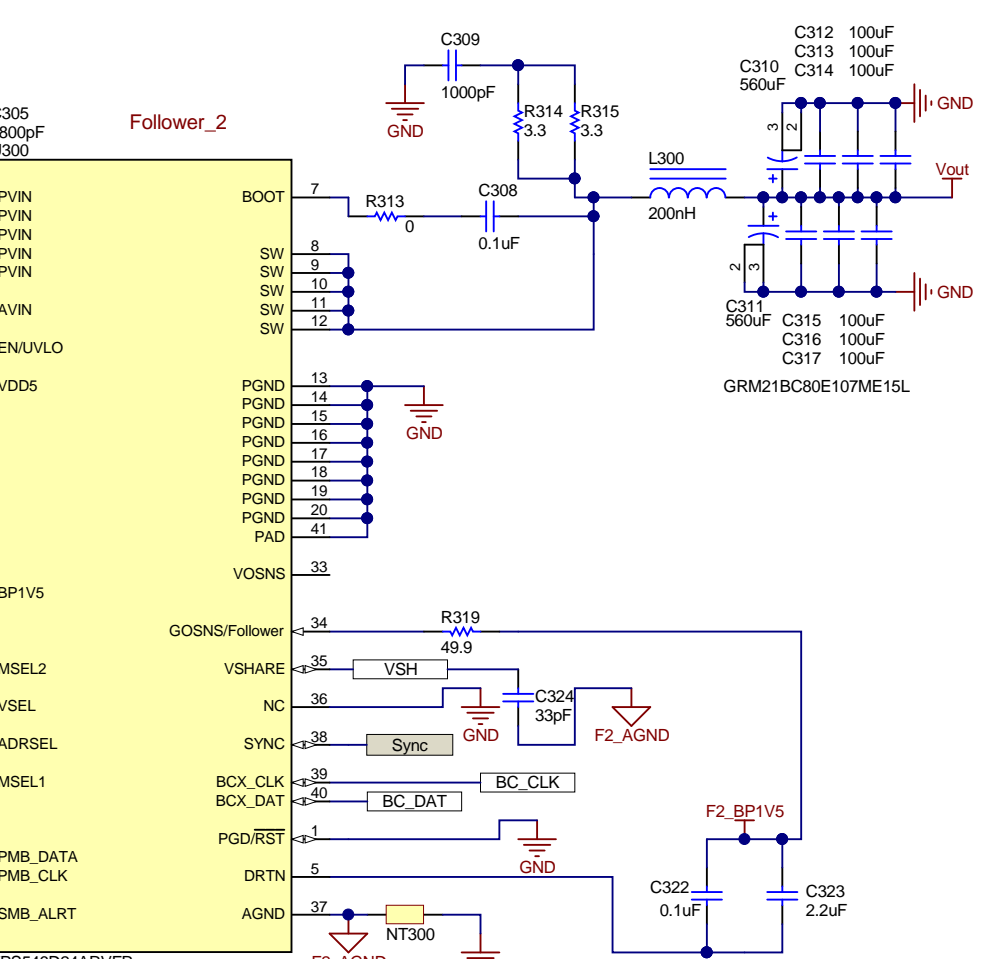
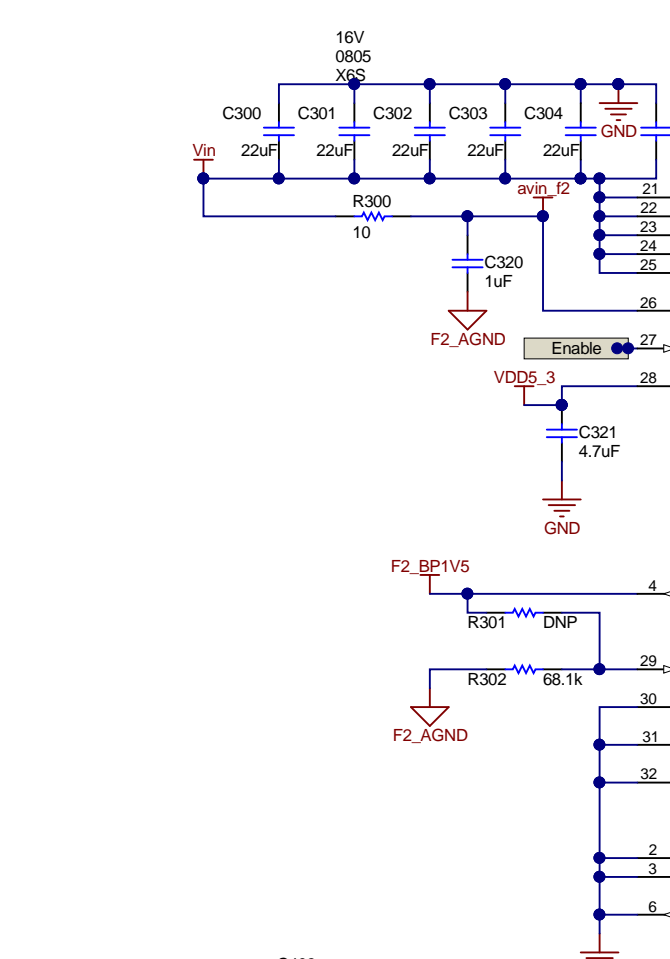
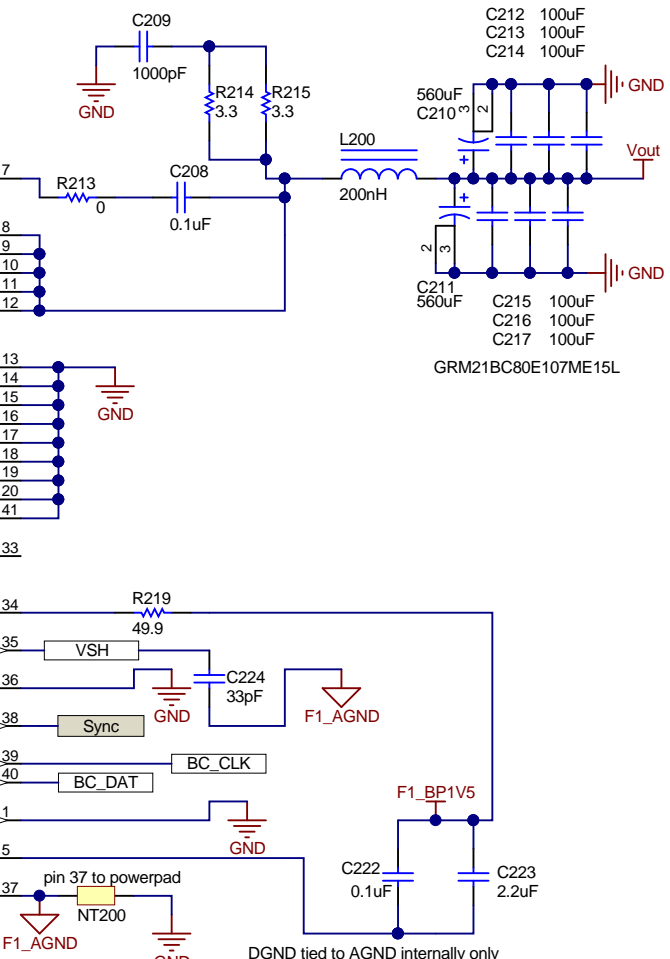
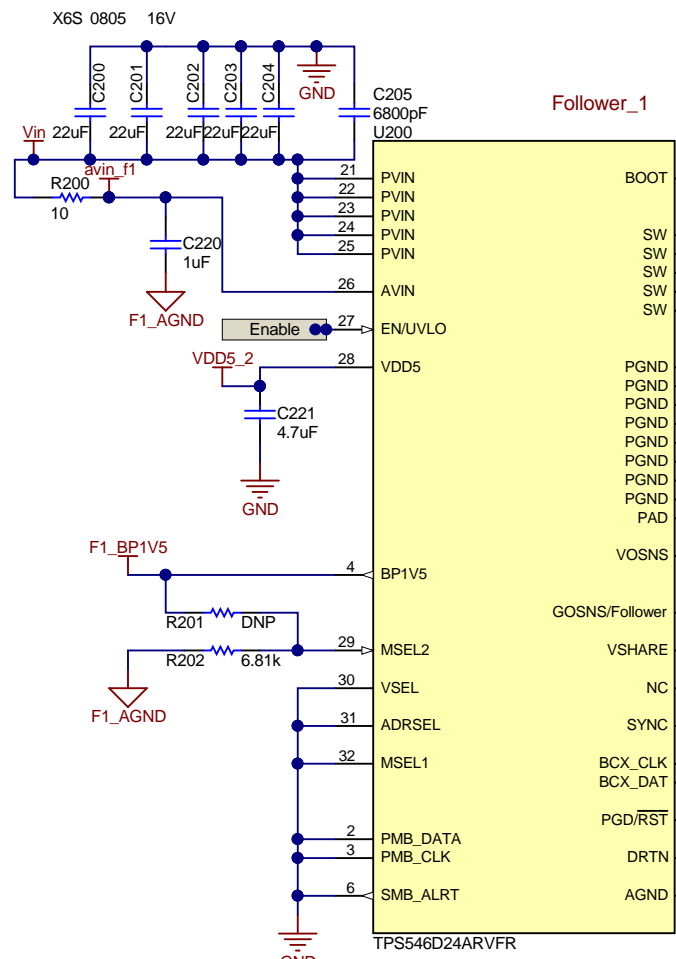
Remote Sense P up to 1.2V 200A peak Dynamic Load including Q500 is part of Test Interface, not the design itself



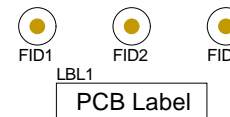
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TID #: N/A	Project Title: TPS546D24A 4 Phases Compact Design	
Number: PMP21254	Rev: B	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: [No Variations]	Sheet: 2 of 3
Drawn By:	File: PMP21254B_Leader&Aux.SchDoc	Size: B
Engineer: Josh Mandelcorn	Contact: http://www.ti.com/support	





Variant/Label Table	
Variant	Label Text
001	ChangeMe!
002	ChangeMe!



PCB
LOGO
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PCB Number: PMP21254
PCB Rev: B

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Drawn By:	File: PMP21254B_Followers&Hardware.SchDoc	Size: B
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