

TMS320DM816x/TMS320C6A816x/AM389x DDR3 Initialization with Software Leveling

Keshava Murthy, Vaibhav Bedia

ABSTRACT

This article has been contributed to the TI Embedded Processors Wiki. The article describes how to initialize the DDR3 on the TMS320DM816x DaVinci™ digital media processors, TMS320C6A816x Integra™ DSP+ ARM® processors, and AM389x Sitara™ ARM® microprocessors (MPUs) PG1.1 samples using the software leveling process.

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