



DRILL CHART					
ALL UNITS ARE IN MILS					
FIGURE	SIZE	PLATED	QTY	TOL.	NOTE
1	8.0	PLATED	26		
2	8.0	PLATED	48		
3	10.0	PLATED	118		
4	10.0	PLATED	1677		
5	12.0	PLATED	16		
6	13.0	PLATED	6		
7	38.0	PLATED	33		
8	40.0	PLATED	38		
9	51.0	PLATED	4		
10	125.0	PLATED	10		
11	138.0	PLATED	2		
12	40.0	NON-PLATED	2		
13	47.0	NON-PLATED	4		
14	50.0	NON-PLATED	2		
15	62.0	NON-PLATED	2		
16	63.0	NON-PLATED	1		
17	40.0	PLATED	1		
18	40.0	PLATED	2		
19	40.0	PLATED	1		

- TSLK
- SMC
- LAYER 1
- LAYER 2
- LAYER 3
- LAYER 4
- LAYER 5
- LAYER 6
- LAYER 7
- LAYER 8
- LAYER 9
- LAYER 10
- LAYER 11
- LAYER 12
- SMS
- BSLK

LAYER SCHEDULE  
SCALE: NONE

9. IMPEDANCE CONTROL: SINGLE ENDED 50 OHMS; 5 MIL TRACES. ALL LAYERS. 100 OHM DIFFERENTIAL +/-10%; 5.1 MIL TRACE/4.9 MIL SPACE.
8. SURFACE FINISH SHALL BE IMMERSION GOLD.
7. FINISHED BOARD SHALL BE FREE OF BURRS ON EDGES.
6. DRC'S MUST BE RUN ON THE GERBERS BEFORE BUILDING BOARDS UNLESS PRIOR APPROVAL IS GIVEN IN WRITING.
5. PLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .003 INCH. UNPLATED THRU HOLE SIZE TOLERANCES SHALL BE +/- .002 INCH.
4. SOLDERMASK ON BOTH SIDES OF THE BOARD SHALL BE S.M.O.B.C.I. LPI. COLOR: GREEN. MINIMUM CLEARANCE ON THE BOARD SHALL BE .005 OF AN INCH. MINIMUM TRACE WIDTH ON THE BOARD SHALL BE .005 OF AN INCH.
3. COPPER PLATED SURFACE CONDUCTORS SHALL BE THE EQUIVALENT OF 1 OZ. COPPER AFTER PLATING. PLANE LAYERS SHALL BE 1 OZ. COPPER. AVERAGE COPPER ON THE WALL OF THE PLATED HOLE SHALL BE .001 INCH.
2. TOTAL BOARD THICKNESS SHALL BE .062 +/- .007 INCHES. ALL MATERIAL SHALL BE NEVA GRADE FR-4 OR EQUIVALENT, TG>=170C.
1. NOTES: UNLESS OTHERWISE SPECIFIED

DATE	9-24-14	APPROVED		SPECTRUM DIGITAL
DESIGN ENGR		CHECKED		FABRICATION DRAWING.
PROJECT ENGR		DRAFTING		VAVU VISION BOARD
ENGR MGR		ENGR		SCALE NONE
				SHEET 1 OF 1
				516621 REV D

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