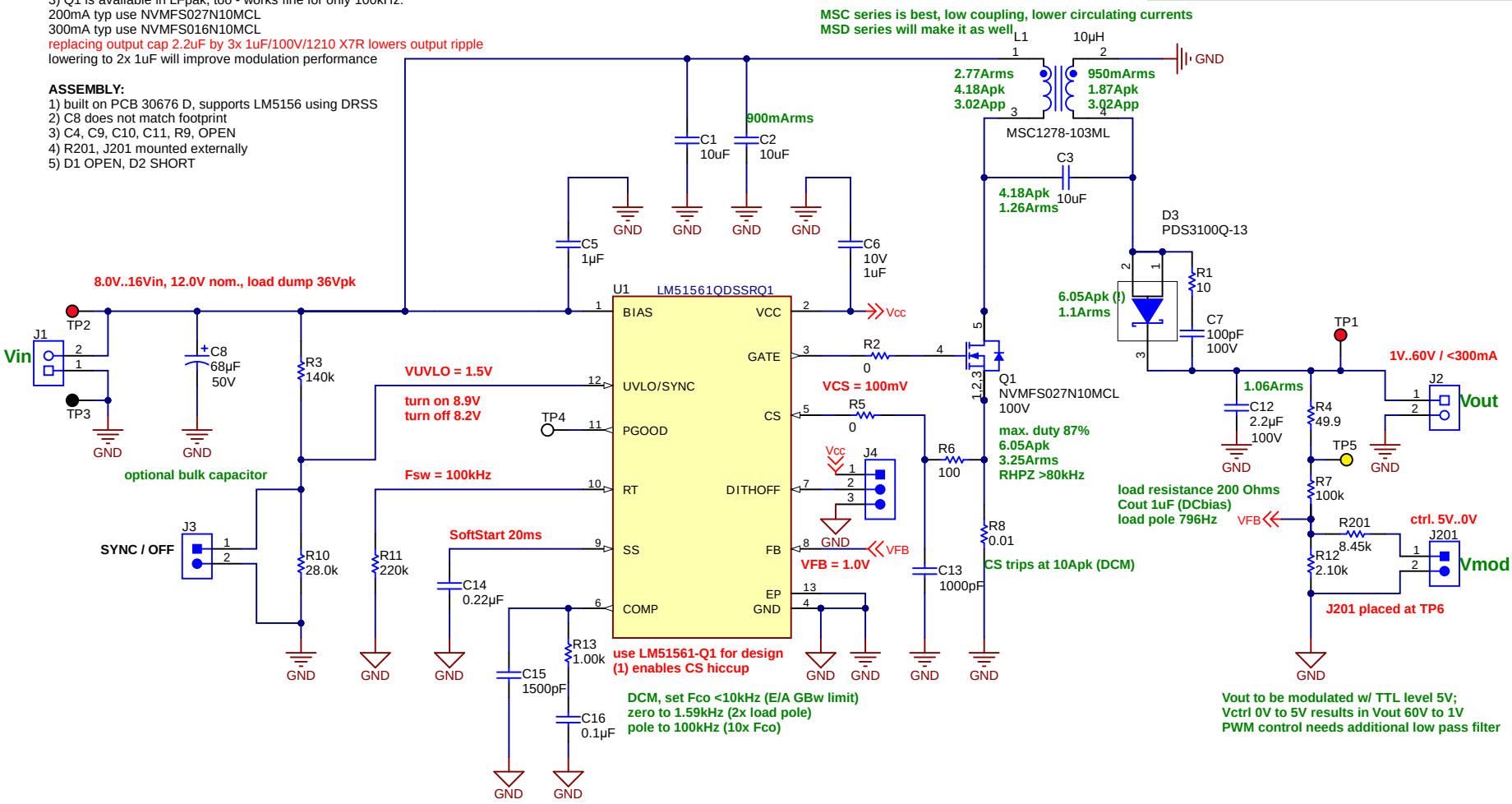
Revision History	
Revision	Notes
A B C	* PRELIMINARY * w/ LM5155 PCB 30676 RevD w/ LM5156, built & tested minimized Cout to modulate up to 120Hz

NOTES:
1) R4 for test purpose only
2) stress analysis for 7Vin and DC output 60V@300mA (J201 SHORT)
3) Q1 is available in LFpak, too - works fine for only 100kHz:
200mA typ use NVMFS027N10MCL
300mA typ use NVMFS016N10MCL
replacing output cap 2.2uF by 3x 1uF/100V/1210 X7R lowers output ripple
lowering to 2x 1uF will improve modulation performance

ASSEMBLY:
1) built on PCB 30676 D, supports LM5156 using DRSS
2) C8 does not match footprint
3) C4, C9, C10, C11, R9, OPEN
4) R201, J201 mounted externally
5) D1 OPEN, D2 SHORT



Vout is modulated (1V..60V) at Vmod (5V..0V, sinusoidal w/ DC offset, up to 120Hz)

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You should completely validate and test your design implementation to confirm the system functionality for your application.

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SVN Rev: Not in version control		Project Title: Auto DCM SEPIC w/ modulated output voltage	
Drawn By:		Sheet Title: PMP31246	
Engineer: B.Geck		Assembly Variant: [No Variations]	Sheet: 1 of 1
		File: PMP31246_RevC_Schematic.SchDoc	Size: A4
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