

ZX1
Assembly Note
VIN = 5 - 40VDC

ZX2
Assembly Note
Fsw = 440kHz

ZX3
Assembly Note
VOUT = 3.3VDC at 10A

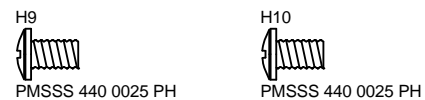
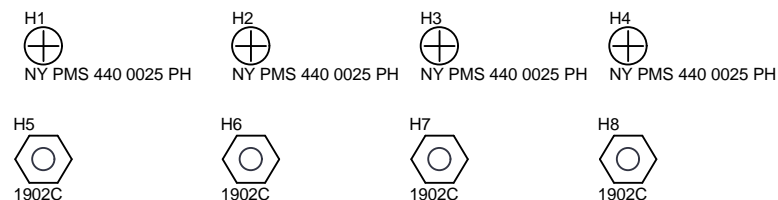
Built on PMP20387 REV A PC Board

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Orderable: N/A	Designed for: Public Release	Mod. Date: 2/28/2017
TID #: PMP20682	Project Title: LM5141 Synchronous Buck	
Number: PMP20682	Rev: B	Sheet Title: LM5141 BUCK REGULATOR EVM
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 2
Drawn By:	File: PMP20682.SchDoc	Size: B
Engineer: R. Sheehan	Contact: http://www.ti.com/support	



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***Caution: When updating PCB from schematics, make sure to un-select the H9 and H10 screws from being updated on the PCB. This is to ensure that these heatsink mounting pads/holes remain connected to (or associated with) the "GND" net, which was performed manually.**



PCB: PMP20387
PMP20387 REV A thermals optimized



PCB LOGO
FCC disclaimer



LBL1
PCB Label

Label Table	
Variant	Label Text
001	
002	

ZZ1
Label Assembly Note
This Assembly Note is for PCB labels only

ZZ2
Assembly Note
These assemblies are ESD sensitive, ESD precautions shall be observed.

ZZ3
Assembly Note
These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.

ZZ4
Assembly Note
These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.

ZZ5
Assembly Note
Install Shunt Jumper SH-J1 onto J5

ZZ6
Assembly Note
Install Shunt Jumper SH-J3 onto J4 pin 2-3.

ZZ7
Assembly Note
Install Shunt Jumper SH-J4 onto J6 pins 1-2.

ZZ8
Assembly Note
Install Shunt Jumper SH-J6 onto J3.

Orderable: N/A	Designed for: Public Release	Mod. Date: 1/10/2017
TID #: PMP20682	Project Title: LM5141 Synchronous Buck	
Number: PMP20682	Rev: B	Sheet Title: LM5141 EVM HARDWARE
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 2 of 2
Drawn By: R. Sheehan	File: PMP20682-HW.SchDoc	Size: B
Engineer: R. Sheehan	Contact: http://www.ti.com/support	

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